i.MX 93 Applications Processor Family – Arm® Cortex®-A55, ML Acceleration, Power Efficient MPU

i.MX93

Last Updated: May 6, 2024

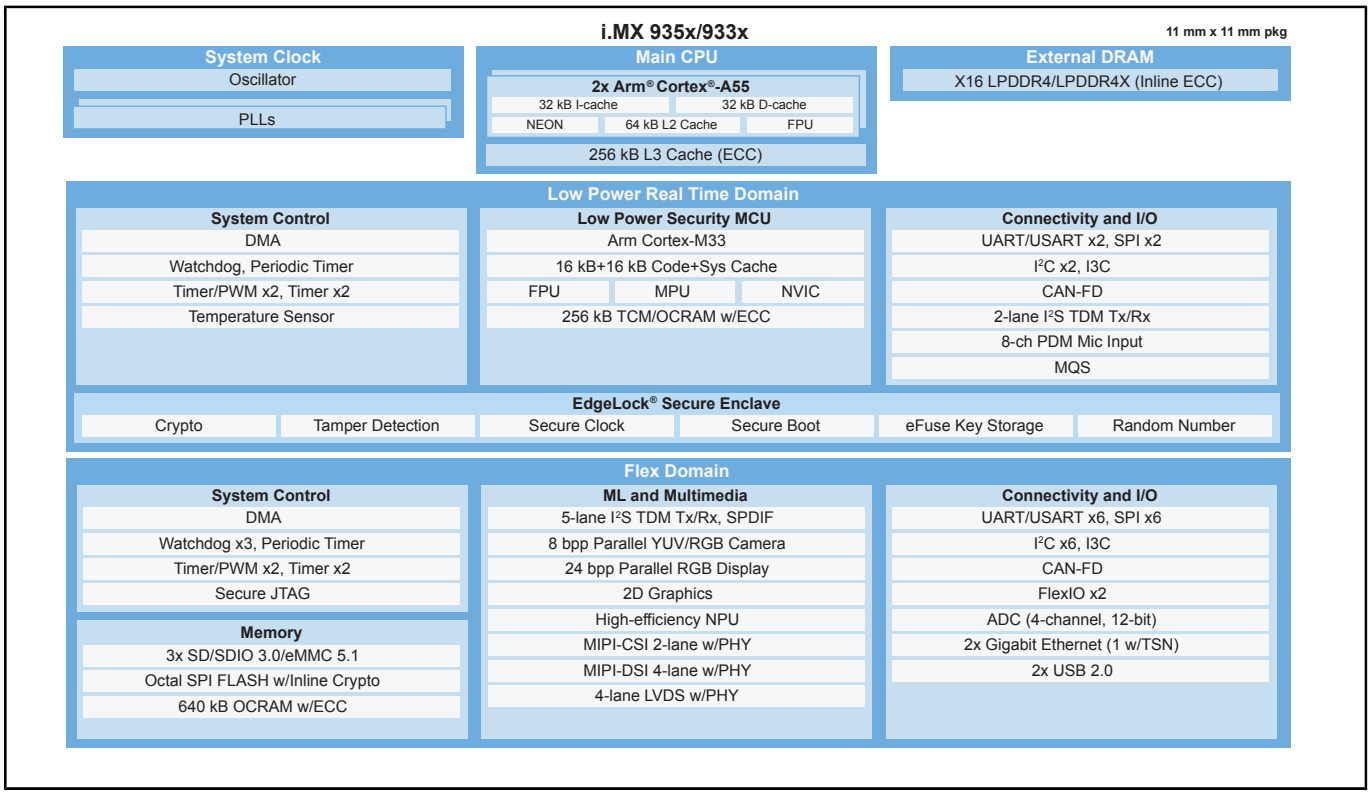
i.MX 93 applications processors deliver efficient machine learning (ML) acceleration and advanced security with integrated EdgeLock® secure enclave to support energy-efficient edge computing.

The i.MX 93 applications processors are the first in the i.MX portfolio to integrate the scalable Arm Cortex-A55 core, bringing performance and energy efficiency to Linux®-based edge applications and the Arm Ethos™-U65 microNPU, enabling developers to create more capable, cost-effective and energy-efficient ML applications.

Optimizing performance and power efficiency for Industrial, IoT and automotive devices, i.MX 93 processors are built with NXP's innovative Energy Flex architecture. The SoCs offer a rich set of peripherals targeting automotive, industrial and consumer IoT market segments.

Part of the EdgeVerse™ portfolio of intelligent edge solutions, the i.MX 93 family will be offered in commercial, industrial, extended industrial and automotive level qualification and backed by NXP's product longevity program.

i.MX 93 Processors Family Block Diagram



View additional information for i.MX 93 Applications Processor Family – Arm® Cortex®-A55, ML Acceleration, Power Efficient MPU.

Note: The information on this document is subject to change without notice.