



# S32K3 Microcontrollers for Automotive General Purpose

## S32K3

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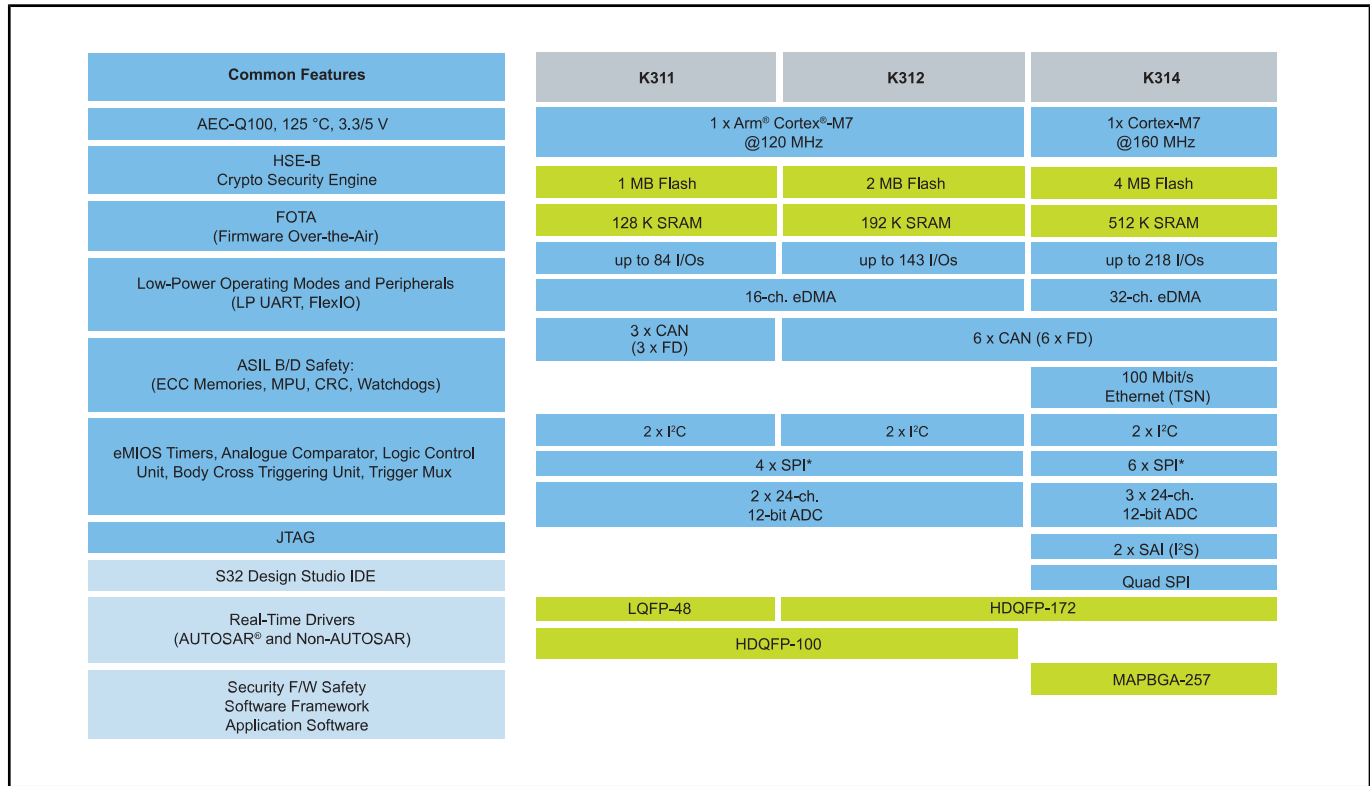
The S32K3 Family of 32-bit microcontrollers (MCUs) offers Arm® Cortex®-M7-based MCUs in single, dual and lockstep core configurations. S32K3 Family offers scalability in number of cores, memory and peripherals, ensuring high-performance and functional safety compliant with ISO 26262 up to ASIL D.

S32K3 Family provides a comprehensive end-to-end solution for development and production. S32K3 MCUs feature a hardware security engine (HSE) with NXP firmware, enable firmware over-the-air (FOTA) updates and include ISO 26262 compliant Real-Time Drivers (RTD) suitable for both AUTOSAR® and non-AUTOSAR applications.

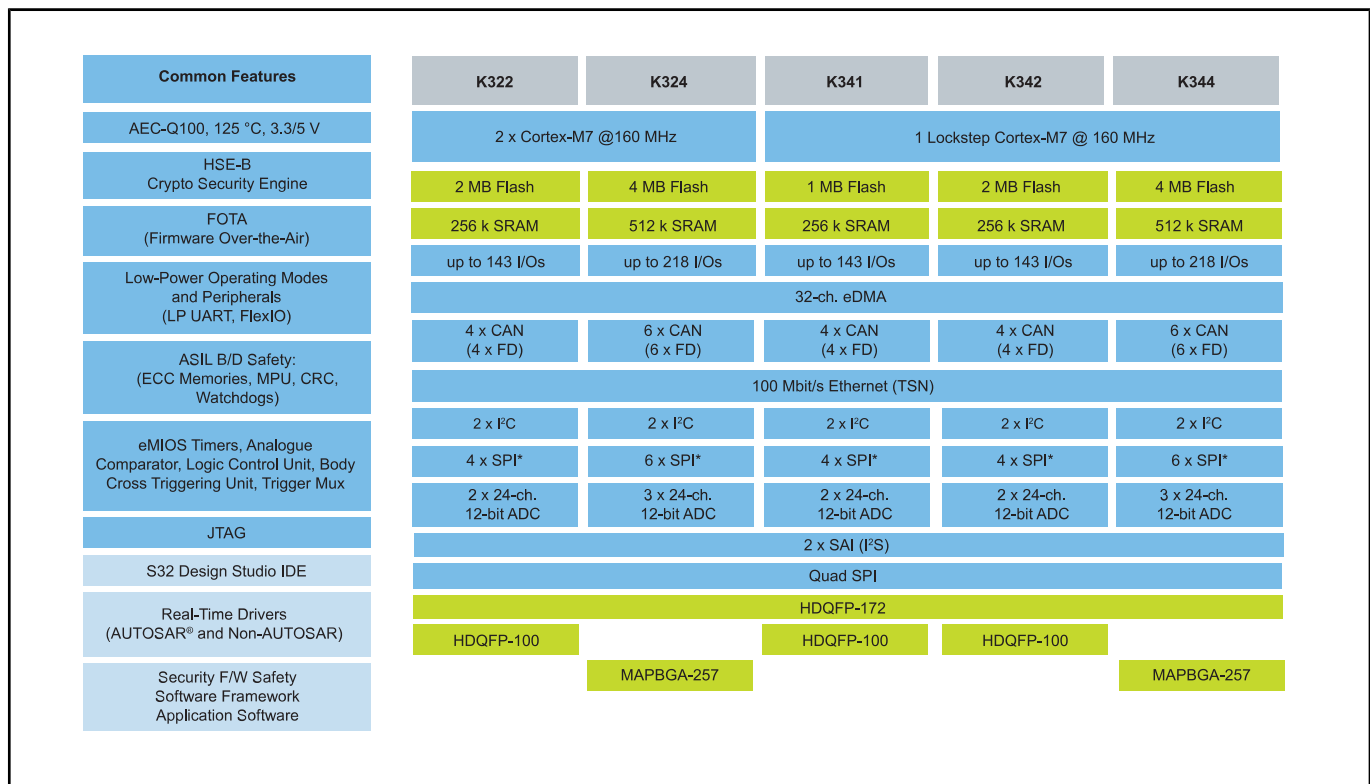
Through compatibility with the NXP S32 Automotive Platform, S32K3 Family enables seamless software reuse and flexibility across applications in body, zone control and electrification.

S32K3 MCUs are available in various package types, including MAPBGA, LQFP and HDQFP, providing flexibility in integration and design. Also, the NXP HDQFP package reduces the package footprint by up to 55 % compared to a standard QFP package.

## S32K3 Family Features Part 1 Block Diagram



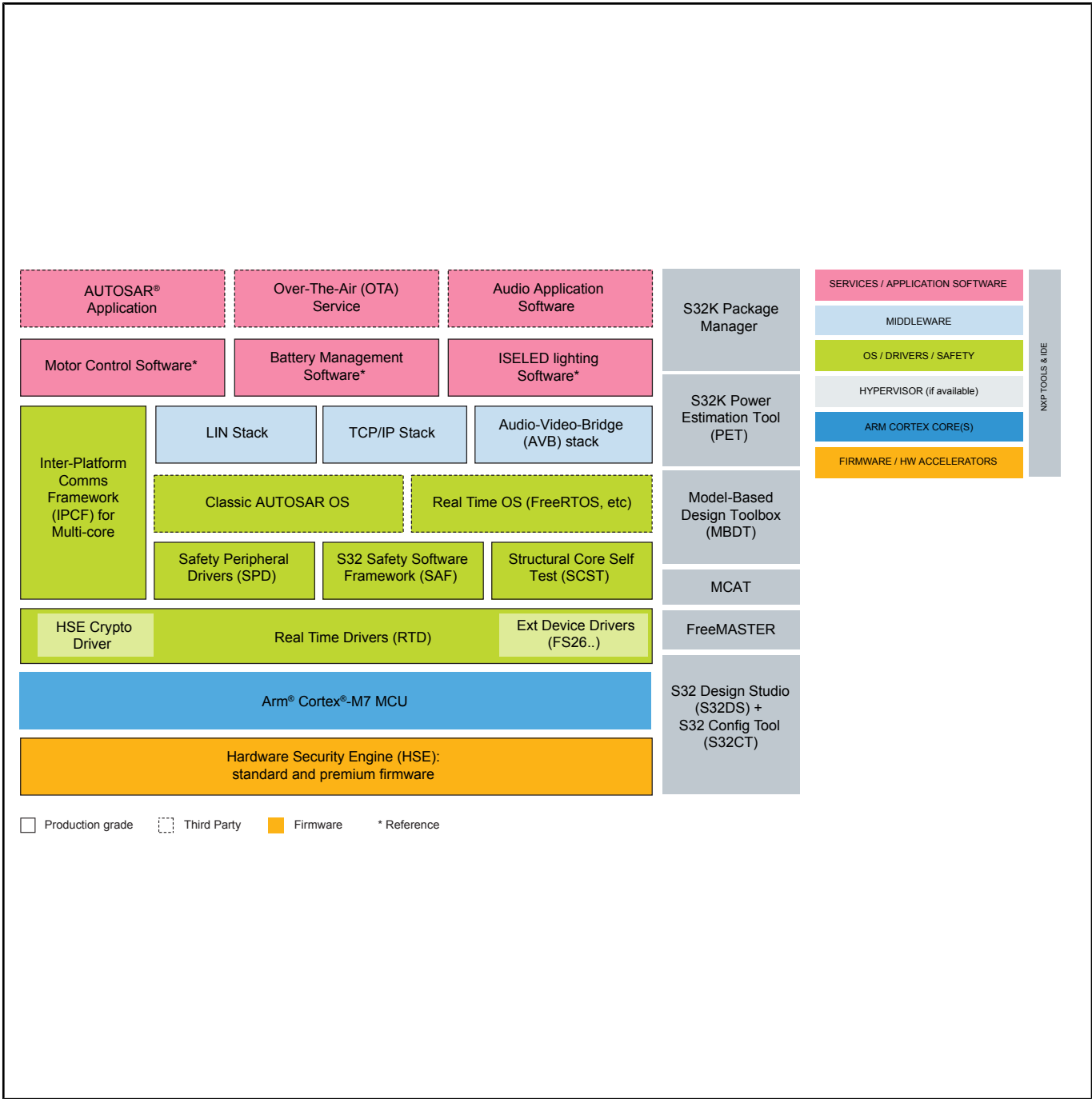
## S32K3 Family Features Part 2 Block Diagram



## S32K3 Family Features Part 3 Block Diagram

Common Features	K328	K338	K348	K358
AEC-Q100, 125 °C, 3.3/5 V	2 x Cortex-M7 @ 240 MHz	3 x Cortex-M7 @ 240 MHz	1 LS Cortex-M7 @ 240 MHz	1 LS Cortex-M7 + 1 Cortex-M7 @ 240 MHz
HSE-B Crypto Security Engine	8 MB Flash			
FOTA (Firmware Over-the-Air)	1152 KB SRAM	1152 KB SRAM	1152 KB SRAM	1152 KB SRAM
Low-Power Operating Modes and Peripherals (LP UART, FlexIO)	up to 218 I/Os			
ASIL B/D Safety: (ECC Memories, MPU, CRC, Watchdogs)	32-ch. eDMA			
eMIOS Timers, Analogue Comparator, Logic Control Unit, Body Cross Triggering Unit, Trigger Mux	8 x CAN (8 x FD)	8 x CAN (8 x FD)	8 x CAN (8 x FD)	8 x CAN (8 x FD)
JTAG	1 Gbit/s Ethernet (TSN)			
S32 Design Studio IDE	2 x I <sup>2</sup> C			
Real-Time Drivers (AUTOSAR® and Non-AUTOSAR)	6 x SPI*			
Security F/W Safety Software Framework Application Software	3 x 24-ch. 12-bit ADC			
	2 x SAI (I <sup>2</sup> S)			
	Quad SPI + SDHC (SDIO)			
	HDQFP-172			
	MAPBGA-289			

## S32K3 Software Ecosystem Block Diagram



View additional information for [S32K3 Microcontrollers for Automotive General Purpose](#).

**Note:** The information on this document is subject to change without notice.

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