



# 12-Channel Power Management Integrated Circuit (PMIC) for High-Performance Processing Applications

## PF8100-PF8200

Last Updated: Aug 22, 2023

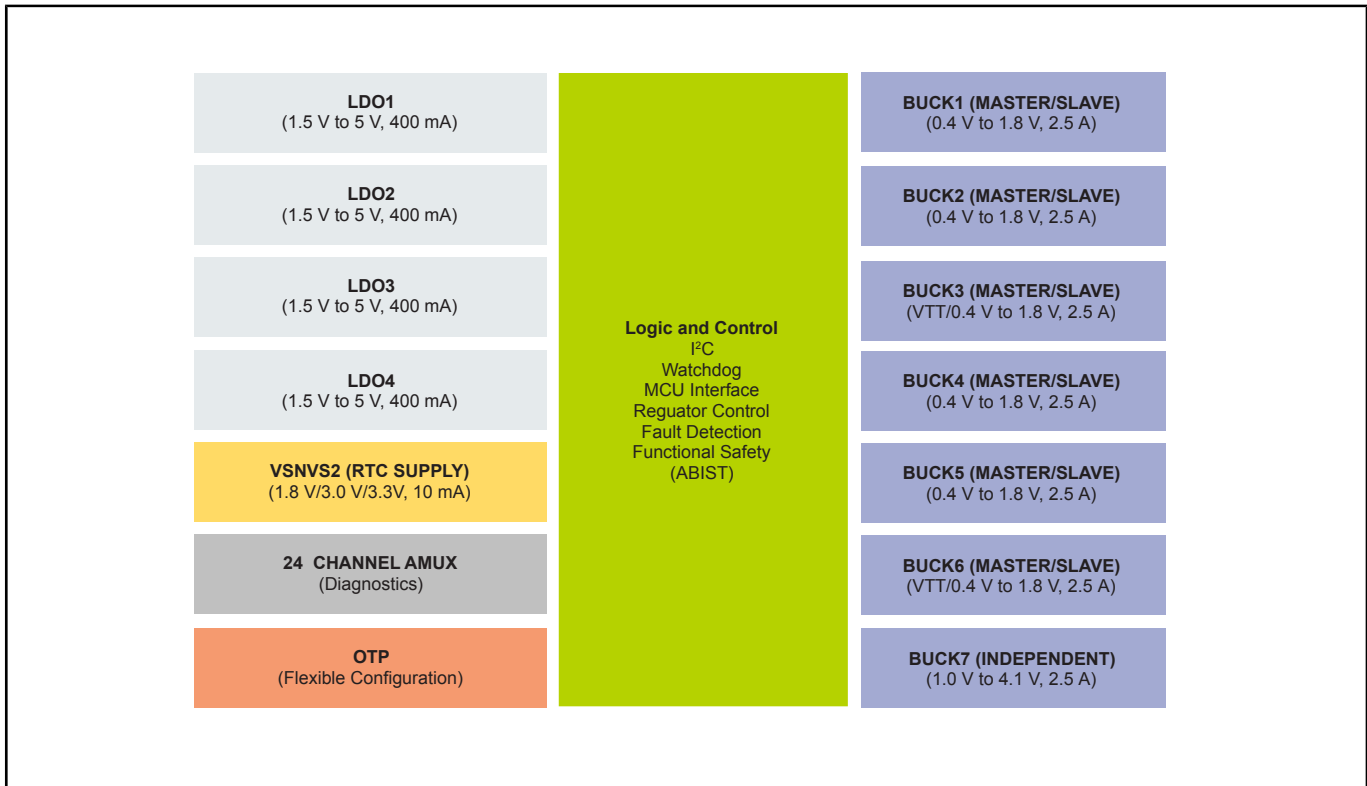
The PF81/PF82 PMIC family is designed for high-performance processing applications such as infotainment, telematics, clusters, vehicle networking, ADAS, vision and sensor fusion.

Two versions are available to address different market needs:

- The PF82 with functional safety to comply with the ISO 26262 standard, provides a powerful and flexible solution for ASILB (D) automotive applications.
- The PF81 is the basic version of this product, with power management and digital control without the functional safety features for systems not requiring ASILB compliance.

This family is ideal for [i.MX 8](#), [i.MX 8X](#) and [S32V](#) processors' based applications and suitable for powering [Layerscape LS1043A](#) and other high-performance processors. It offers seven high-efficiency buck converters and four linear regulators to power the processor, memory and miscellaneous peripherals.

## PF8100 and PF8200 Block Diagram



View additional information for [12-Channel Power Management Integrated Circuit \(PMIC\) for High-Performance Processing Applications](#).

**Note:** The information on this document is subject to change without notice.

[www.nxp.com](http://www.nxp.com)

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.