LPC1549 Xpresso v2 Rev B board Silkscreen errors; Apr 30, 2014

Several errors in the top and bottom side silkscreen incorrectly identify the LPC1549 port pin associated with the header pin. The correct port references are shown (in red) in the diagrams below. The schematic "LPC1549_LPCXpresso v2_schem_Rev_B2.pdf" posted on LPCWare.com is correct. The table below also shows which header or receptacle pins have mis-labeled silkscreen (column 2) and the corresponding correction (column 3).

Table of silkscreen errors.

LPCXpresso1549 Rev B board	Column1	Column2	Column3
Xpresso header	Pin #	incorrect silk	correct silk
P2	8	P0_2	P1_0
P2	23	P0_17	P1_2
P2	47	P0_30	P1_1
Aruduino Rect	Pin #	incorrect silk	correct silk
J1	Digital 9	P0_2	P1_0



