



TOP

BOTTOM

Date	Draw by	Verify	Modifications	IND.
Date : 11/07/16		Material : ...	Marging : +/-0.1	Nbr of layers : 2
Draw by : ASCO-SR		File.		
Checked by: V.FILLATRE		Finition : Ni/AU CHEMICAL	Manufact.level : 5	
Project : JN518X		Protection: SOLDERMASK(BLUE) TOP/BOTTOM	Scale : 1/1	Silkscreen(WHITE) TOP :YES BOTTOM:NO
NXP Semiconductors 2,Esplanade Anton Philips Campus Effiscience Colombelles BP 2000 - 14906 CAEN CEDEX 9			All rights are reserved. Reproduction in whole or in part is prohibited without the written consent of the copyright owner	
ASSEMBLY DRAWING TOP/BOTTOM JN518X MEZZANINE OM15077-1			SIZE A4	PCB2457-1-400
			REV.	PL.
			00	1/1