



# UM11058

## TEA1936xDB1463 45 W USB-PD and Quick Charge demo board

Rev. 1 — 5 February 2018

User manual

### Document information

Information	Content
Keywords	TEA1936xDB1463, TEA1936x universal serial bus, power delivery, USB-PD, quick charge USB-PD 2.0 and 3.0, type-C
Abstract	This user manual describes the performance, technical data, and the connections of the TEA1936xDB1463 demo board. The TEA1936xDB1463 demo board operates at mains voltages from 90 V (AC) up to 264 V (AC) with an output voltage from 5 V (DC) up to 20 V (DC).



## Revision history

Rev	Date	Description
v.1	20180205	first issue

# 1 Introduction

**Warning**



The non-insulated high voltages that are present when operating this product, constitute a risk of electric shock, personal injury, death and/or ignition of fire. This product is intended for evaluation purposes only. It shall be operated in a designated test area by personnel qualified according to local requirements and labor laws to work with non-insulated mains voltages and high-voltage circuits. This product shall never be operated unattended.

This user manual describes the operation of the TEA1936xDB1463 demo board. The TEA1936xDB1463 features the quasi-resonant controller TEA19361, the synchronous rectifier controller TEA1993, and the USB-PD Type-C and QC2.0/3.0 controller TEA19051B.

The TEA1936xDB1463 demo board is designed for delivering a maximum output power of 45 W at a maximum current of 3 A. Output voltages can be chosen from 5 V up to 20 V.

The TEA1936xDB1463 provides an effective solution with a low current ripple and high efficiency for USB-PD and quick charge applications.

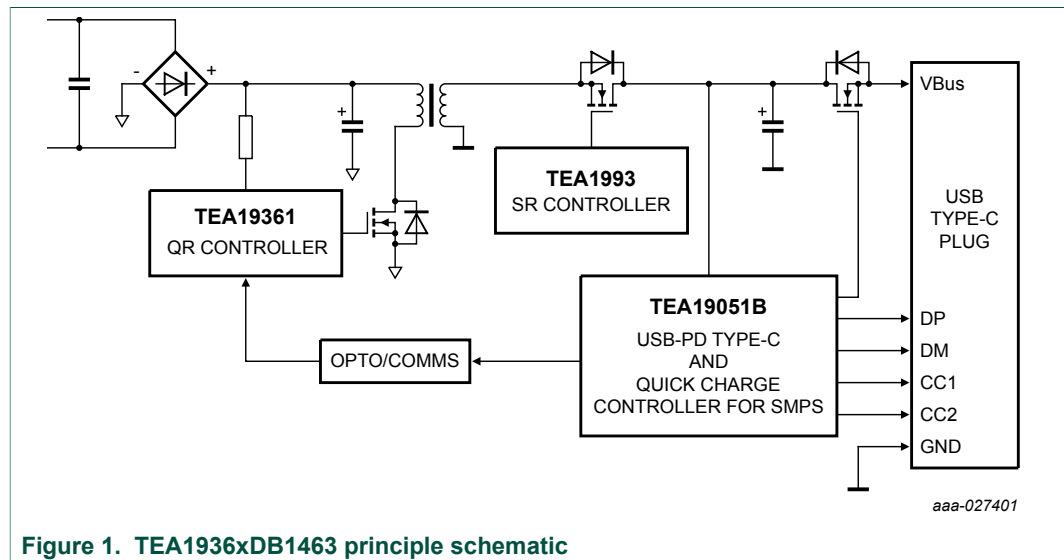


Figure 1. TEA1936xDB1463 principle schematic

## 1.1 Key features

- Multi-protocol support for USB-PD 2.0 and 3.0, Quick Charge 2.0 and 3.0
- Functionality user-configurable end of line
- Best-in-class energy efficiency meeting all DOE & EU CoC requirements
- < 30 mW no-load power, low audible noise, low output voltage ripple
- Small size due to high near-full digital integration level and > 15.9 W/CI power density
- Best-in-class Thermal management
- Safe solution with extensive set of hardware-integrated protection features
- Complete one-stop-shop solution from NXP Semiconductors minimizing development time and research and development cost

## 1.2 Applications

Mobile chargers with Type-C connector for:

- Mobile phones
- Smart phones
- Tablets
- Notebooks

The new smart charger platform of NXP Semiconductors helps designers of travel adapters to maximize power output for the smallest form-factor with a short bill of materials.

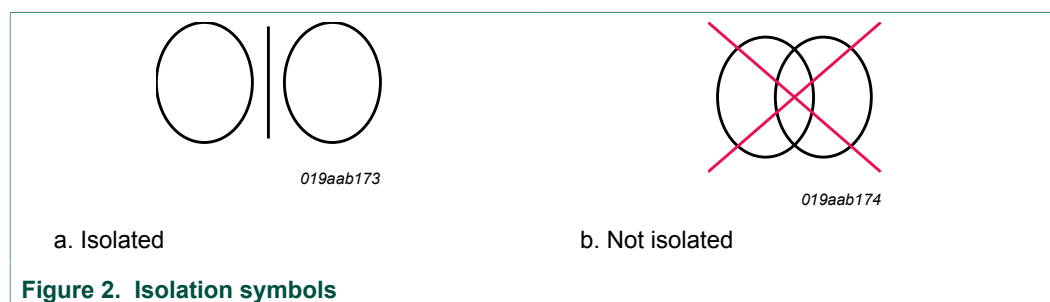
The result is a cost-effective design. It meets the requirements published by Energy Star, the Department of Energy (DoE) in the United States, the Ecodesign Directive of the European Union, the European Code of Conduct, and other guidelines.

Supporting hardware (UTC) and software (GUI) for USB-PD available for jump-starting application.

## 2 Safety warning

The TEA1936xDB1463 demo board is connected to the mains voltage. Avoid touching the board while it is connected to the mains voltage and when it is in operation. An isolated housing is obligatory when used in uncontrolled, non-laboratory environments. Galvanic isolation from the mains phase using a fixed or variable transformer is always recommended.

Figure 2 shows the symbols on how to recognize these devices.

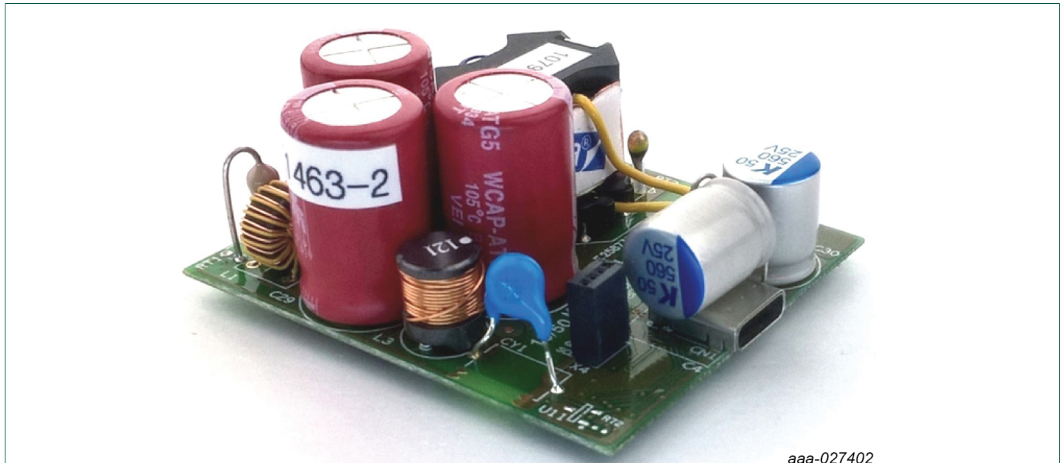


## 3 Specifications

**Table 1. TEA1936xDB1486 specifications**

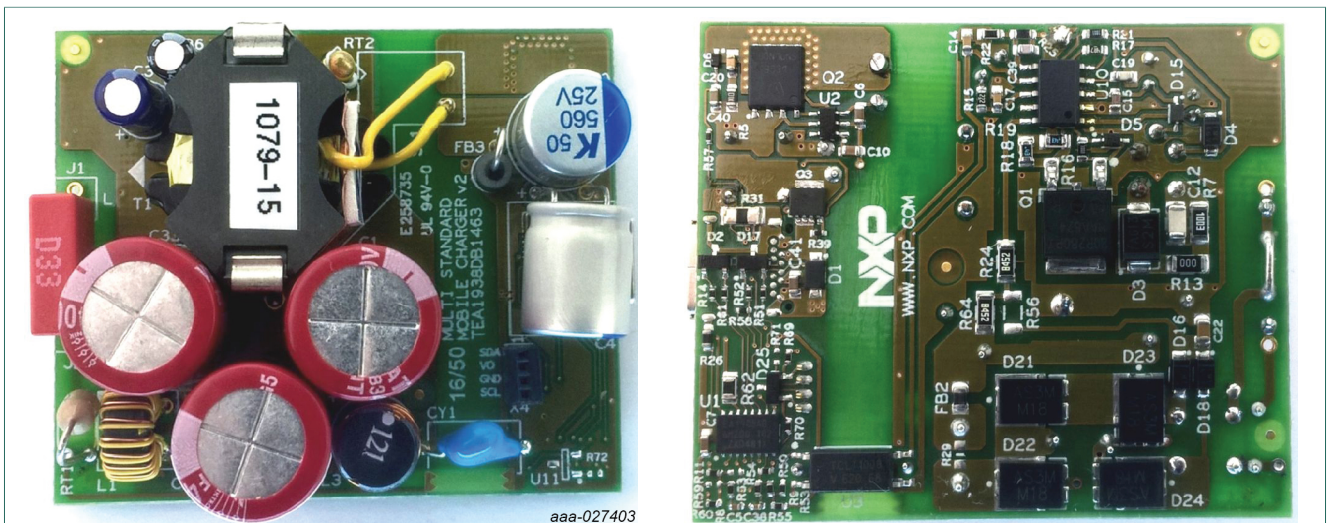
Symbol	Parameter	Value
$V_{\text{mains}}$	AC mains voltage	90 V (AC) up to 264 V (AC)
$P_{\text{out(max)}}$	maximum output power	45 W
$f_{\text{mains}}$	mains frequency	47 Hz to 63 Hz
$P_{\text{idle}}$	no-load input power	< 30 mW
$\eta$	efficiency	> 92 %; at $P_{\text{out(max)}}$
$V_{\text{out}}$	output voltage	5 V (DC) to 20 V (DC)
$I_{\text{out(max)}}$	maximum output current	3 A
$V_{\text{ripple(burst)}}$	output voltage ripple in burst mode	100 mV (p-p); at cable end
$V_{\text{ripple(full)}}$	output voltage ripple at continuous switching	80 mV (p-p); at cable end
EMI	conducted EMI	-7 dB
CMN	common-mode noise	< 2 V (p-p)
ESD	electrostatic discharge	$\pm 15$ kV; through air
		$\pm 8$ kV; via contact

4 Board photographs



aaa-027402

Figure 3. TEA1936xDB1463 demo board



aaa-027403

aaa-027404

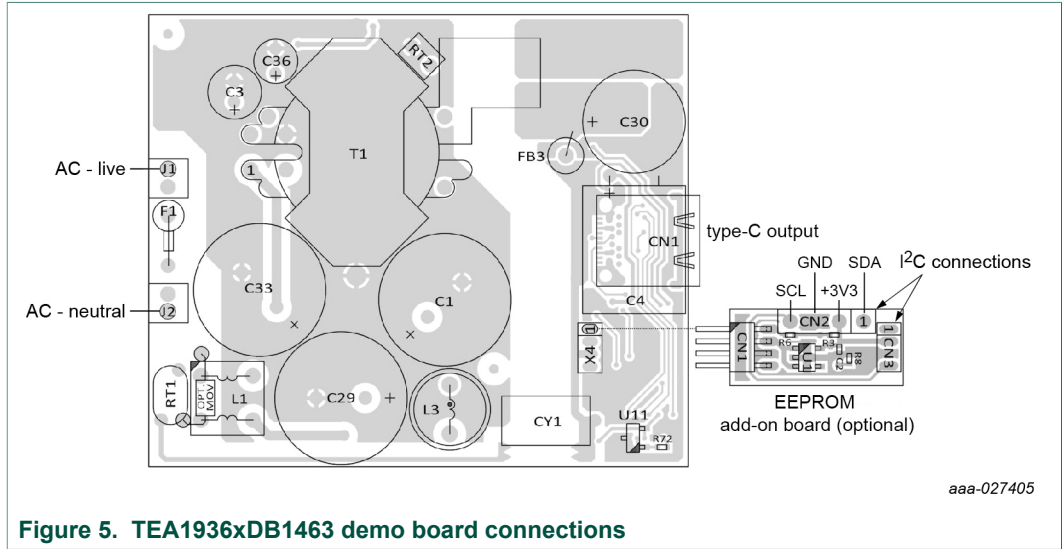
a. Top view

b. Bottom view

Figure 4. TEA1936xDB1463

## 5 TEA1936xDB1463 demo board connections

The TEA1936xDB1463 demo board is a universal mains supplied application. The output is the Type-C receptacle. Setting the output voltage is done through the USB type-C interface. Additionally, the TEO II graphical user interface software provides the option to program other output voltages and limit currents into the TEA19051B PD controller IC. [Section 7](#) lists the default settings.



## 6 TEA1936xDB1463 demo board performance

### 6.1 Efficiency

Table 2. Efficiency at 5 V output (PCB end)

Load	Efficiency at 115 V (AC)(%)	Efficiency at 230 V (AC)(%)
10 % (0.3 A)	87.8	84.4
25 % (0.75 A)	88.6	85.7
50 % (1.5 A)	89.3	86.6
75 % (2.25 A)	90	86.9
100 % (3 A)	90.1	88.9
4-point average	89.5	87

Table 3. Efficiency at 9 V output (PCB end)

Load	Efficiency at 115 V (AC)(%)	Efficiency at 230 V (AC)(%)
10 % (0.3 A)	88.5	86.1
25 % (0.75 A)	90.0	87.6
50 % (1.5 A)	91.2	89.0
75 % (2.25 A)	92.5	90.7
100 % (3 A)	92.4	92.1
4-point average	91.5	89.8

Table 4. Efficiency at 15 V output (PCB end)

Load	Efficiency at 115 V (AC)(%)	Efficiency at 230 V (AC)(%)
10 % (0.3 A)	86.5	84.8
25 % (0.75 A)	88.8	87.7
50 % (1.5 A)	91.8	90.5
75 % (2.25 A)	93.2	92.0
100 % (3 A)	92.9	93.0
4-point average	91.7	90.8



**Table 5. Efficiency at 20 V output (PCB end)**

Load	Efficiency at 115 V (AC)(%)	Efficiency at 230 V (AC)(%)
10 % (0.23 A)	86.1	84.2
25 % (0.575 A)	88.9	87.5
50 % (1.15 A)	91.8	89.9
75 % (1.725 A)	92.8	92.3
100 % (2.3 A)	93.0	92.8
4-point average	91.6	90.6

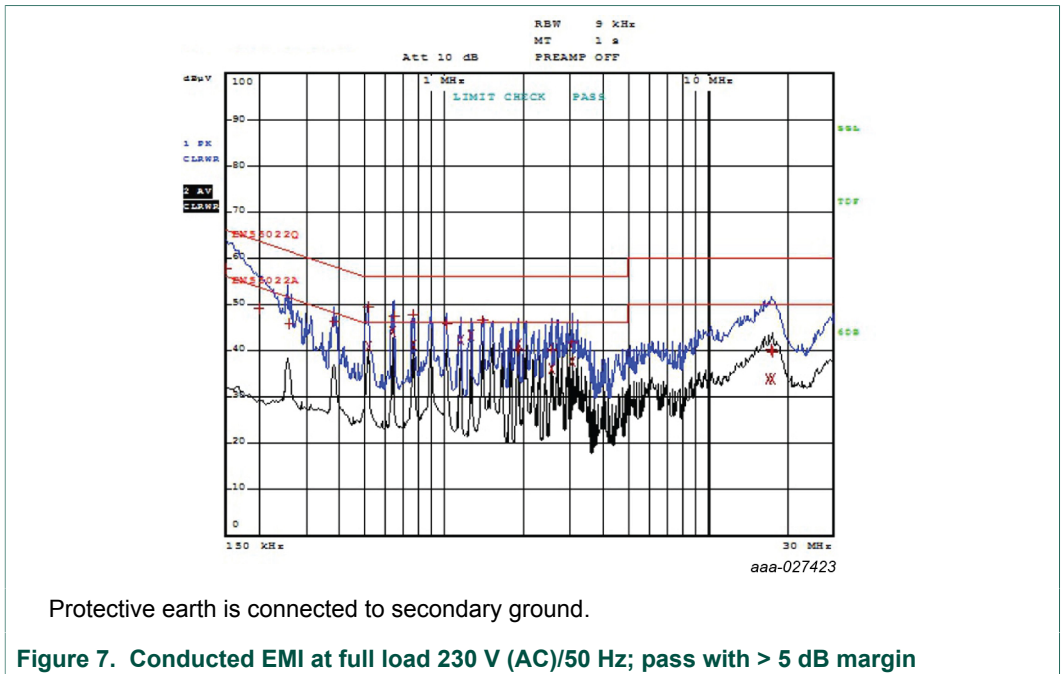
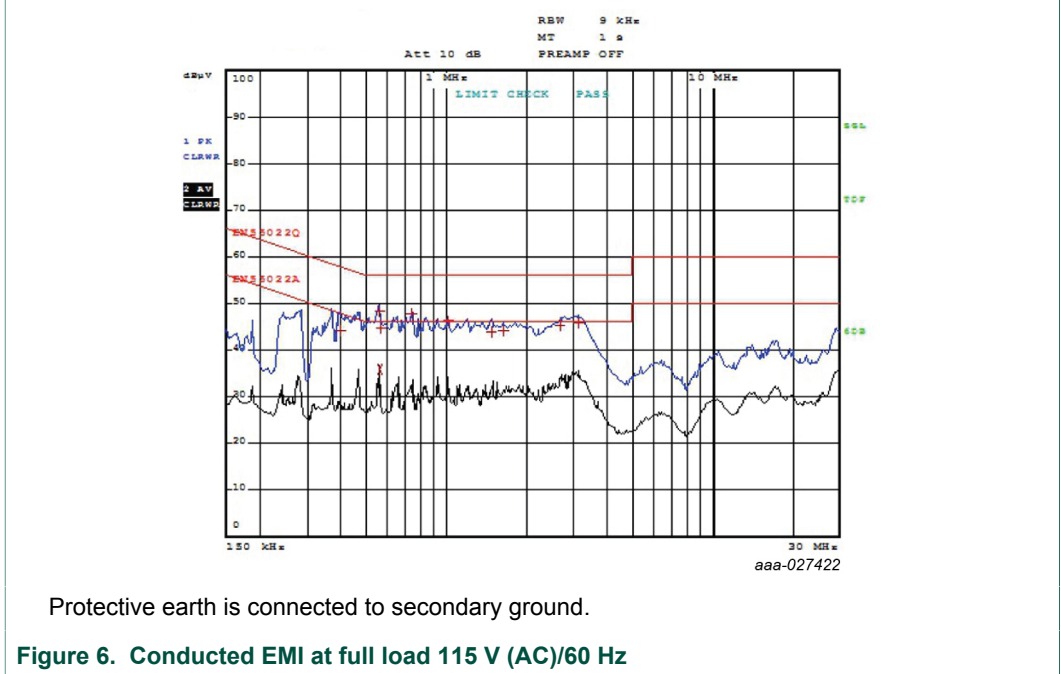
## 6.2 No-load power consumption at 5 V output

**Table 6. No-load power consumption**

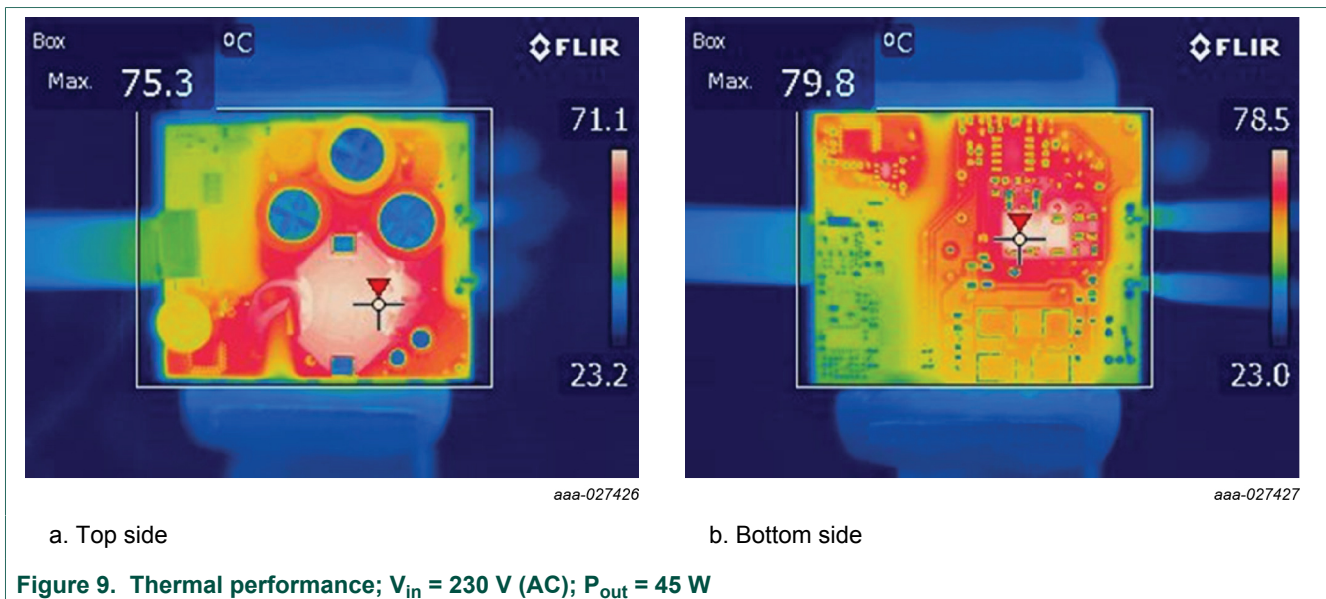
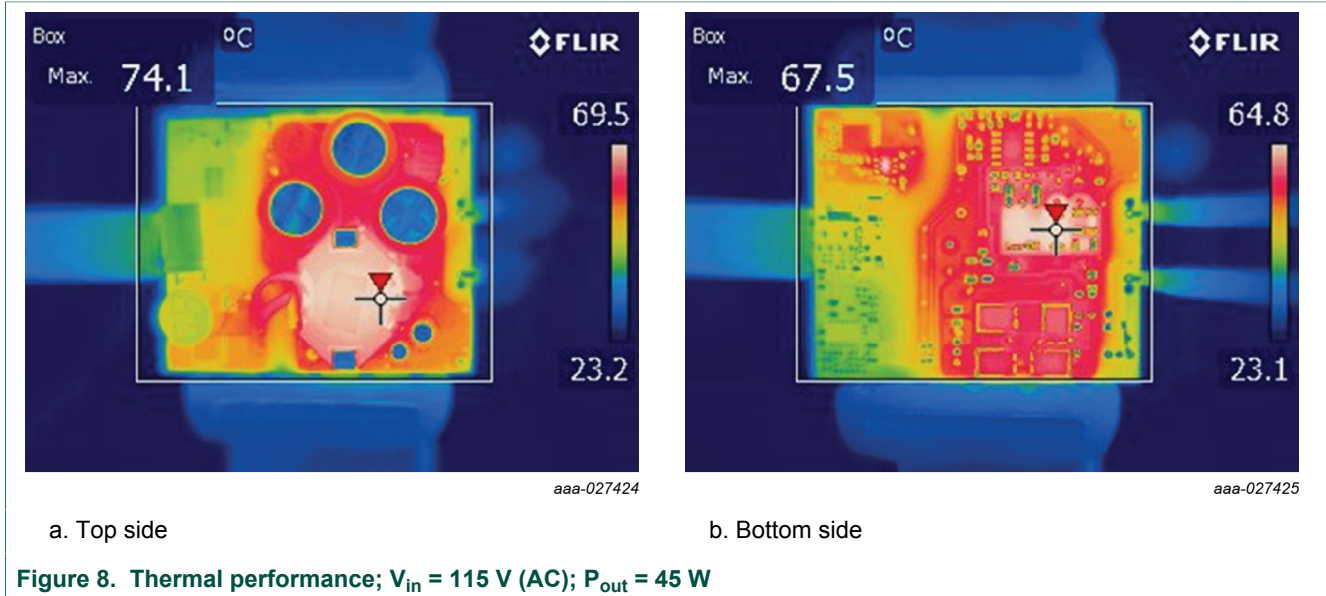
*Type-C cable disconnected.*

Input voltage (V (AC))	Input frequency (Hz)	No-load power (mW)
90	60	21.1
115	60	21.6
150	60	22.2
180	50	22.3
200	50	22.9
230	50	23.9
264	50	28.4

### 6.3 ElectroMagnetic Interference (EMI)



6.4 Thermal



**Note:** Typical temperature distribution at  $T_{amb} = 25 \text{ °C}$ ; PCB in free air, natural convection, and radiation only. For the TEA1936xDB1463 demo board, the voltage levels must not exceed 20 V also. Current limit levels must not exceed 3 A. Power limit levels must not exceed 45 W.

## 7 PDO settings

[Table 7](#) shows the settings of the output voltages and currents for the efficiency measurement.

**Table 7. PDO settings**

Default values for demo board TEA1936xDB1463

PDO (#)	V <sub>out</sub> (V)	I <sub>out</sub> (A)
1	5	3
2	9	3
3	15	3
4	20	2.3

PDO settings can be changed by reprogramming the MTP settings of the TEA19051B via UTC using the TEO-II software (see the TEA190x Evaluation Overdrive (TEO) user manual ([Ref. 1](#))).

**Note:** The PDOs must have an ascending voltage and power in order to work correctly. So,  $V(\text{PDO}, i + 1) > V(\text{PDO}, i)$  and also  $P(\text{PDO}, i + 1) > P(\text{PDO}, i)$ . Also, for the TEA1936xDB1463 demo board, the voltage levels must not exceed 20 V. Current limit levels must not exceed 3 A. Power limit levels must not exceed 45 W.

## 8 Schematic

The schematic of the TEA1936xDB1463 comprises the TEA19361 quasi-resonant controller, the TEA1993 synchronous rectifier controller, and the TEA19051B USB-PD/QC controller.

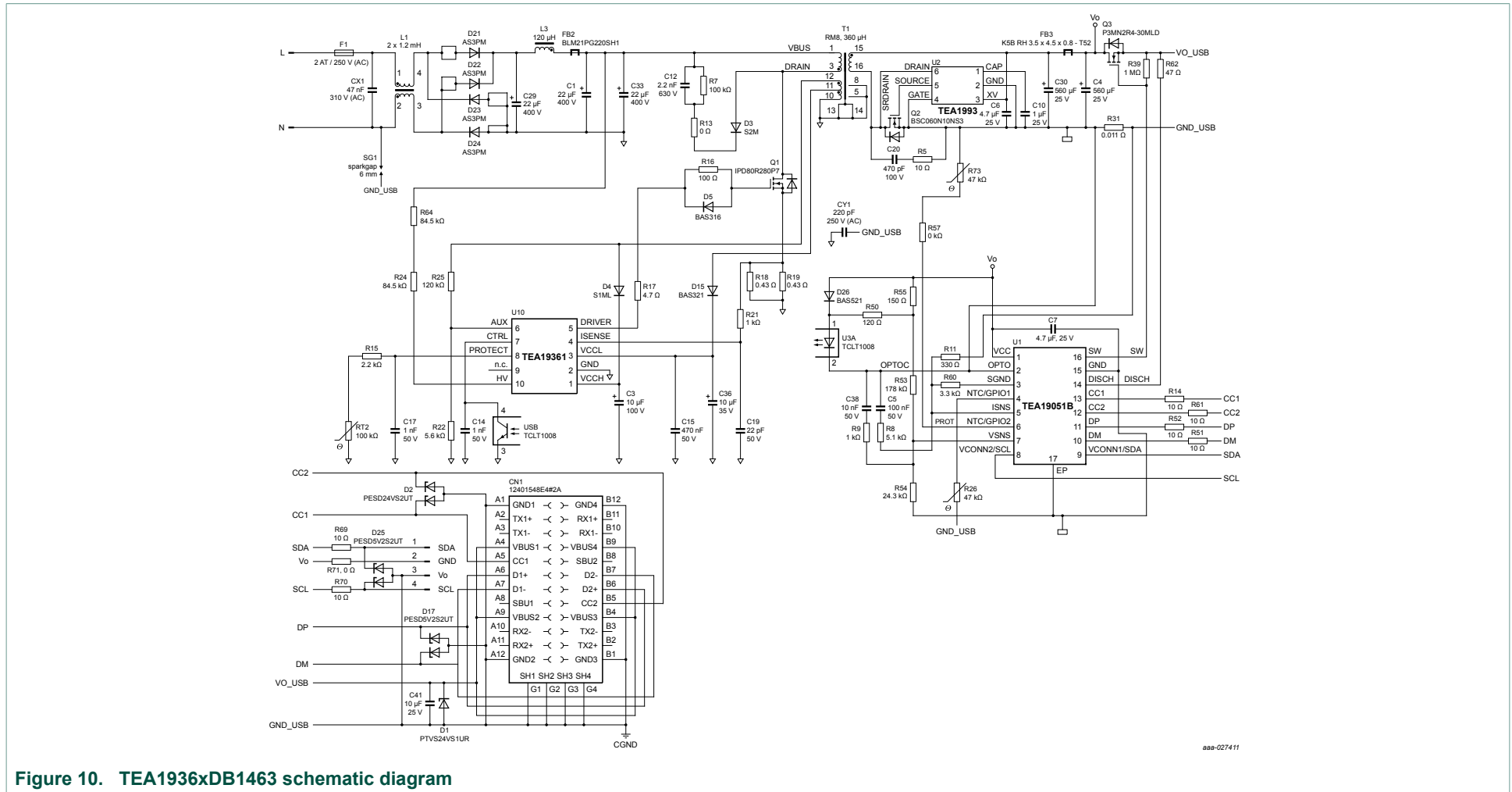
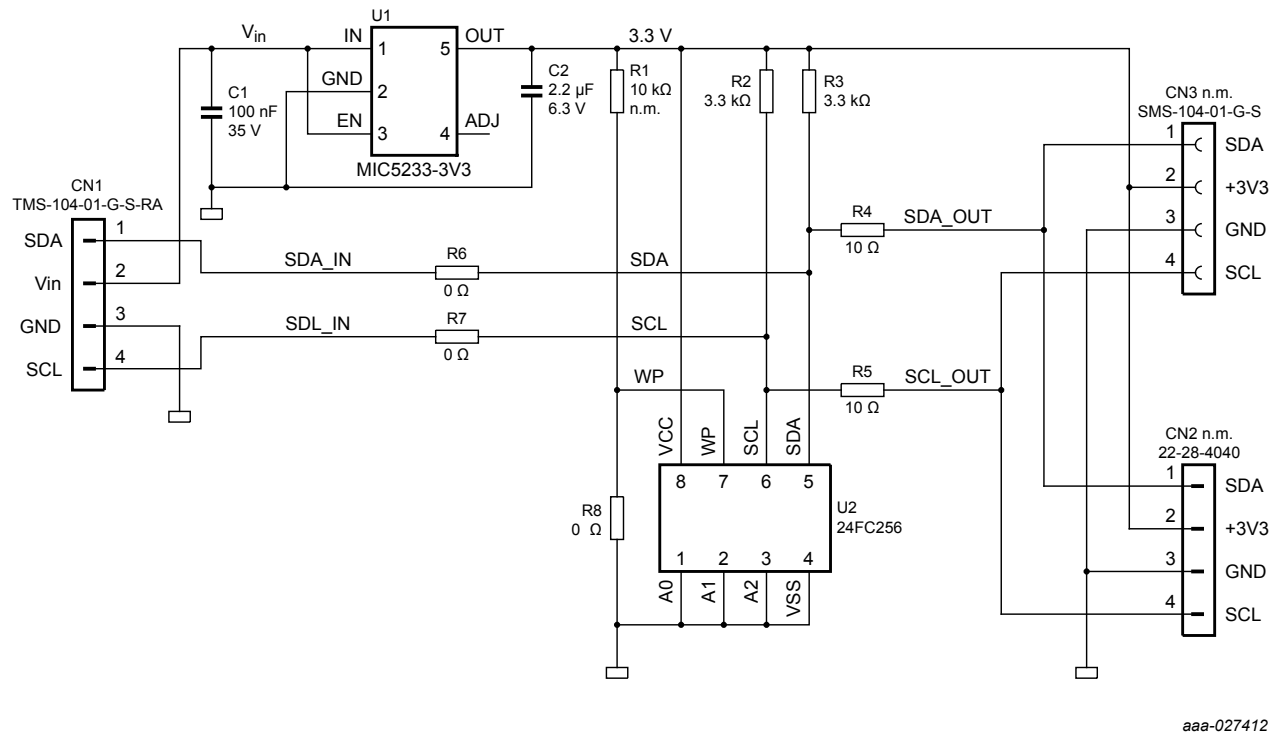


Figure 10. TEA1936xDB1463 schematic diagram



n.m. = not mounted

aaa-027412

Figure 11. Optional EEPROM demo board EEPROM\_DB1418 with I<sup>2</sup>C connections

## 9 Bill Of Materials (BOM)

Table 8. TEA1936xDB1463 bill of materials

Reference	Description and values	Part Number	Manufacturer
C1	capacitor; 22 $\mu$ F; 20 %; 400 V; ALU; D13xL17mm	EKM226M2GJ17RR	SAMXON
C3	capacitor; 10 $\mu$ F; 20 %; 100 V; ALU; THT	100YXJ10M5X11	Rubycon
C4	capacitor; 560 $\mu$ F; 20 %; 25 V; ALU; THT	A750MS567M1EAAE015	KEMET
C5	capacitor; 100 nF; 10 %; 50 V; X7R; 0402	C1005X7R1H104K050BB	TDK
C6	capacitor; 4.7 $\mu$ F; 10 %; 25 V; X5R; 0603	C1608X5R1E475K080AC	TDK
C7	capacitor; 4.7 $\mu$ F; 10 %; 16 V; X5R; 0603	C1608X5R1C475K080AC	TDK
C10	capacitor; 1 $\mu$ F; 10 %; 25 V; X7R; 0603	-	-
C12	capacitor; 2200 pF; 10 %; 630 V; X7R; 1206	C1206C222KBRAC	KEMET
C14	capacitor; 1 nF; 10 %; 50 V; X7R; 0603	-	-
C15	capacitor; 470 nF; 10 %; 50 V; X7R; 0603	C1608X7R1H474K	TDK
C17	capacitor; 1 nF; 10 %; 50 V; X7R; 0603	-	-
C19	capacitor; 22 pF; 5 %; 50 V; C0G; 0603	-	-
C20	capacitor; 470 pF; 10 %; 100 V; X7R; 0603	-	-
C29	capacitor; 22 $\mu$ F; 20 %; 400 V; ALU; D13xL17mm	EKM226M2GJ17RR	SAMXON
C30	capacitor; 560 $\mu$ F; 20 %; 25 V; ALU; THT	A750MS567M1EAAE015	KEMET
C33	capacitor; 22 $\mu$ F; 20 %; 400 V; ALU; D13xL17mm	EKM226M2GJ17RR	SAMXON
C36	capacitor; 10 $\mu$ F; 20 %; 35 V; ALU; THT	UVR1V100MDD6TP	Nichicon
C38	capacitor; 10 nF; 10 %; 50 V; X7R; 0402	-	-
C41	capacitor; 10 $\mu$ F; 10 %; 25 V; X5R; 0603	C1608X5R1E106M080	TDK
CN1	Connector; USB 3.1 Type-C receptacle R/A	12401548E4#2A	Amphenol
CY1	capacitor; 220 pF; 10 %; 250 V (AC); B; THT; X1/Y2	DE2B3KY221KA2BM01F	Murata
D1	diode; TVS; unidirectional; 24 V; 400 W	PTVS24VS1UR	NeXperia USA Inc
D2	diode; ESD protection; 24 V; 3 A	PESD24VS2UT	NeXperia USA Inc
D3	diode; 1 kV; 2 A	S2M	Fairchild
D4	diode; 1 kV; 1 A	S1ML	Taiwan Semiconductor
D5	diode; 100 V; 250 mA	BAS316	NeXperia USA Inc
D15	diode; 200 V; 250 mA	BAS321	NeXperia USA Inc
D17; D25	diode; ESD protection; 30 kV; 3 A	PESD5V2S2UT	NXP Semiconductors
D21; D22; D23; D24	diode; 1 kV; 3 A	AS3PM-M3/86A	Vishay
D26	diode; 300 V; 250 mA	BAS521	NeXperia USA Inc
F1	fuse; slow blow; 250 V; 2 A	MCPMP2A250V	Multicomp
FB2	ferrite bead; 0.009 $\Omega$ ; 6 A; 0805	BLM21PG220SH1D	Murata
FB3	ferrite bead; K5B RH 3.5 $\times$ 4.5 $\times$ 0.8	K5B RH 3.5 x 4.5 x 0.8 - T52	King Core Electronics Inc.

Reference	Description and values	Part Number	Manufacturer
L1	inductor CM; 2 × 1.2 mH; Cu = 0.27 mm; 18T:18T	T8x4x4 H5C3	TDK
L3	inductor; 120 µH; 850 mA; 0.22 Ω	744772121	Würth Elektronik
Q1	MOSFET-N; 800 V; 0.28 Ω; 17 A	IPD80R280P7ATMA1	Infineon
Q2	MOSFET-N; 100 V; 90 A; 0.006 Ω; TDSON	BSC060N10NS3GATMA1	Infineon
Q3	MOSFET-N; 30 V; 2.4 mΩ; 70 A	PSMN2R4-30MLD	NeXperia USA Inc
R5	resistor; 10 Ω; 1 %; 63 mW; 0603	-	-
R7	resistor; 100 kΩ; 1 %; 660 mW; 1206	ERJP08F1003V	Panasonic
R8	resistor; 5.1 kΩ; 1 %; 63 mW; 0402	-	-
R9	resistor; 1 kΩ; 1 %; 63 mW; 0402	-	-
R11	resistor; 330 Ω; 1 %; 63 mW; 0402	-	-
R13	resistor; jumper; 0 Ω; 250 mW; 1206	-	-
R14; R51; R52; R61	resistor; 22 Ω; 1 %; 100 mW; 0402	ERJ2RKF22R0X	Panasonic
R15	resistor; 2.2 kΩ; 1 %; 63 mW; 0603	-	-
R16	resistor; 100 Ω; 1 %; 100 mW; 0603	-	-
R17	resistor; 4.7 Ω; 1 %; 100 mW; 0603	-	-
R18; R19	resistor; 0.43 Ω; 1 %; 250 mW; 0805	ERJS6QFR43V	Panasonic
R21	resistor; 1 kΩ; 1 %; 63 mW; 0603	-	-
R22	resistor; 5.6 kΩ; 1 %; 63 mW; 0603	-	-
R24	resistor; 84.5 kΩ; 1 %; 660 mW; 500 V; 1206	ERJP08F8452V	Panasonic
R25	resistor; 120 kΩ; 1 %; 250 mW; 250 V; 0603	ERJPA3F1203V	Panasonic
R26	resistor; NTC; 47 kΩ; 5 %; 180 mW; 3980 K	B57321V2473J060	EPCOS
R31	resistor; 0.011 Ω; 1 %; 1 W; 1206	ERJ8CWFR011V	Panasonic
R39	resistor; 1 MΩ; 1 %; 63 mW; 0402	CRCW04021M00FKED	Vishay
R50	resistor; 120 Ω; 1 %; 100 mW; 0402	ERJ2RKF1200X	Panasonic
R53	resistor; 178 kΩ; 1 %; 63 mW; 0402	-	-
R54	resistor; 24.3 kΩ; 1 %; 63 mW; 0402	-	-
R55	resistor; 150 Ω; 1 %; 100 mW; 0402	ERJ2RKF1500X	Panasonic
R57	resistor; 0 Ω; 1 %; 63 mW; 0402	-	-
R60	resistor; 3.3 kΩ; 1 %; 63 mW; 0402	-	-
R62	resistor; 100 Ω; 1 %; 500 mW; 0805	ERJP6WF1000V	Panasonic
R64	resistor; 84.5 kΩ; 1 %; 660 mW; 500 V; 1206	ERJP08F8452V	Panasonic
R69; R70	resistor; 10 Ω; 1 %; 100 mW; 0402	ERJ2RKF10R0X	Panasonic
R71	resistor; jumper; 0 Ω; 100 mW; 0402	ERJP6WF47R0V	Panasonic
R73	resistor; NTC; 47 kΩ; 5 %; 180 mW; 2980 K	B57321V2473J060	EPCOS
RT2	resistor; NTC; 100 kΩ; 5 %; 100 mW; 4190 K	NTCLE100E3104JB0	Vishay
T1	Transformer; RM8; 360 µH	RM8 (TR1083)	NXP Semiconductors

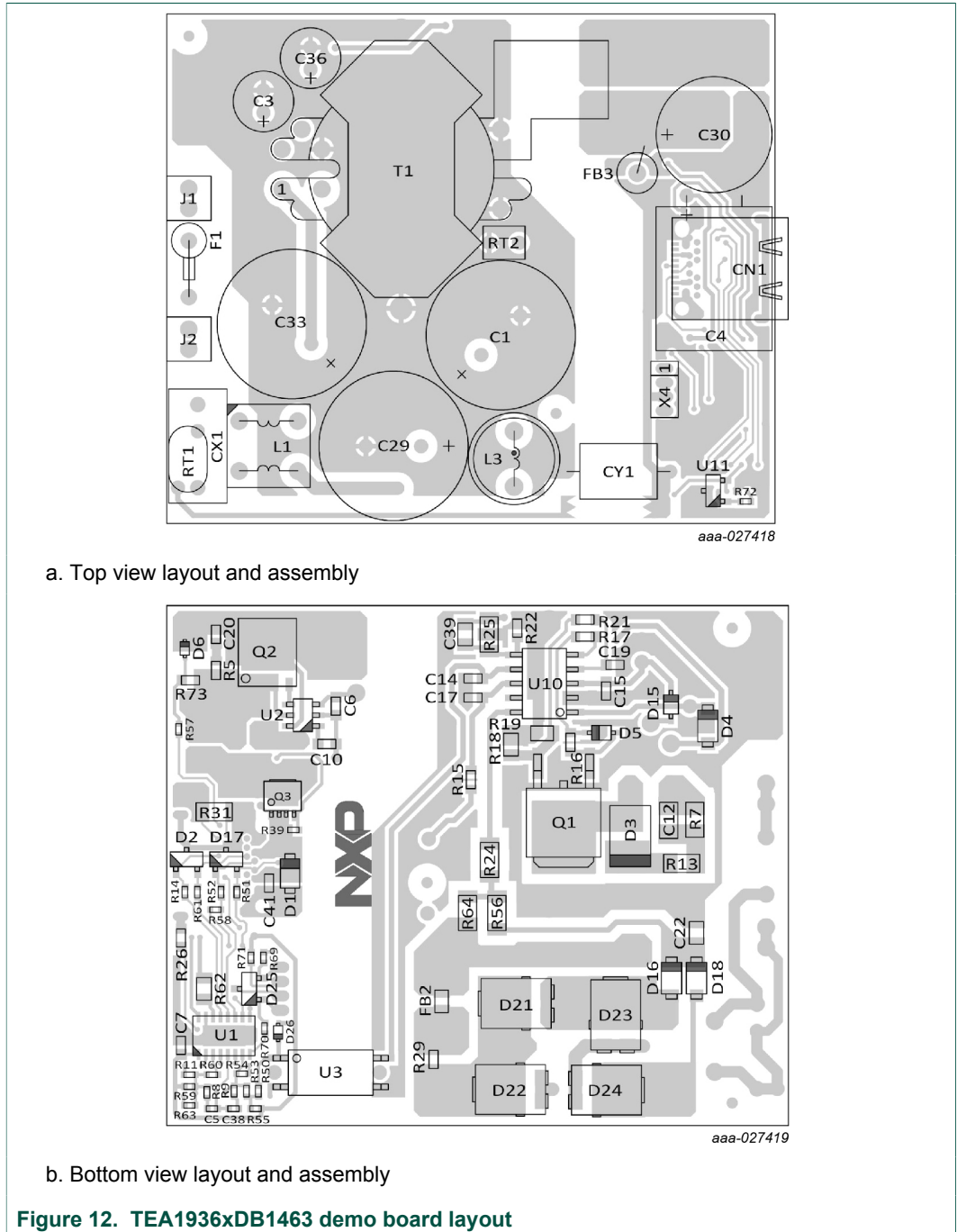


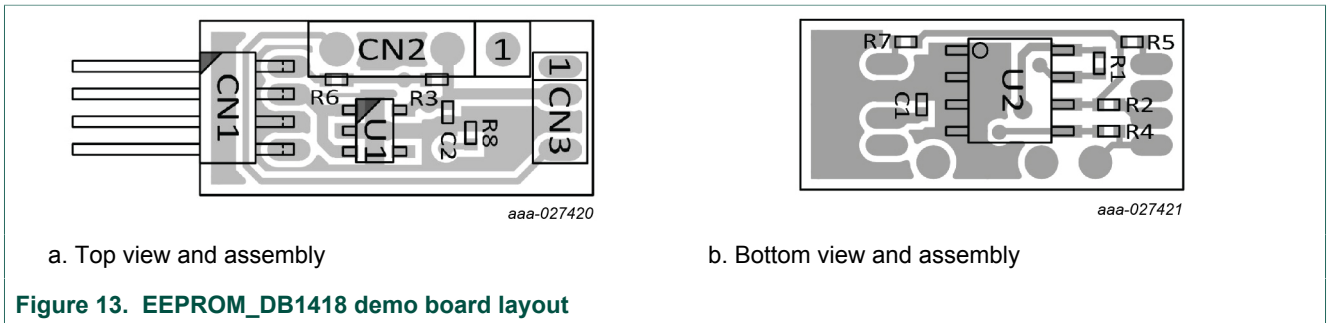
Reference	Description and values	Part Number	Manufacturer
U1	TEA19051B; HVSON16	TEA19051B (HVSON16)	NXP Semiconductors
U2	synchronous rectification controller; TEA1993	TEA1993	NXP Semiconductors
U10	SMTTP controller; TEA19361	TEA19361	NXP Semiconductors
U3	optocoupler; NPN; 70 V; 50 mA	TCLT1008	Vishay

Table 9. EEPROM\_DB1418 bill of materials

Reference	Description and values	Part Number	Manufacturer
C1	capacitor; 100 nF; 10 %; 35 V; X7R; 0402	-	-
C2	capacitor; 2.2 $\mu$ F; 20 %; 6.3 V; X5R; 0402	GRM155R60J225ME15D	Murata
CN1	header; right angle; 1 $\times$ 4-way; 1.27 mm	TMS-104-01-G-S-RA	SAMTEC
CN2	header; not mounted; straight; 1 $\times$ 4-way; 2.54 mm	22-28-4040	Molex
CN3	receptacle; not mounted; straight; 1 $\times$ 4-way; 1.27 mm	SMS-104-01-G-S	SAMTEC
R1	resistor; not mounted; 10 k $\Omega$ ; 1 %; 63 mW; 0402	CRCW040210K0FKED	Vishay
R2	resistor; 3.3 k $\Omega$ ; 1 %; 63 mW; 0402	CRCW04023K30FKED	Vishay
R3	resistor; 3.3 k $\Omega$ ; 1 %; 63 mW; 0402	CRCW04023K30FKED	Vishay
R4	resistor; 10 $\Omega$ ; 1 %; 200 mW; 0402	ERJ-PA2F10R0X	Panasonic
R5	resistor; 10 $\Omega$ ; 1 %; 200 mW; 0402	ERJ-PA2F10R0X	Panasonic
R6; R7; R8	resistor; jumper; 0 $\Omega$ ; 63 mW; 0402	CRCW04020000Z0ED	Vishay
U1	LDO; 3.3 V	MIC5233-3.3YM5 TR	MICREL
U2	EEPROM; 256 kb; I <sup>2</sup> C; CMOS	24FC256-I/SN	Microchip

10 Layout





## 11 Abbreviations

Table 10. Abbreviations

Acronym	Description
USB	Universal Serial Bus
PD	Power Delivery
QC	Quick Charge
UTC	Universal Type-C Controller
PDO	Power Data Object
MTP	Multiple Times Programmable
TEO	TEA190x Evaluation Overdrive

## 12 References

- 1 **UM11014 user manual** TEA190x Evaluation Overdrive (TEO); 2017, NXP Semiconductors

## 13 Legal information

### 13.1 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

### 13.2 Disclaimers

**Limited warranty and liability** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors. In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory. Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use** — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification. Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product

design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products. NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Evaluation products** — This product is provided on an "as is" and "with all faults" basis for evaluation purposes only. NXP Semiconductors, its affiliates and their suppliers expressly disclaim all warranties, whether express, implied or statutory, including but not limited to the implied warranties of non-infringement, merchantability and fitness for a particular purpose. The entire risk as to the quality, or arising out of the use or performance, of this product remains with customer. In no event shall NXP Semiconductors, its affiliates or their suppliers be liable to customer for any special, indirect, consequential, punitive or incidental damages (including without limitation damages for loss of business, business interruption, loss of use, loss of data or information, and the like) arising out of the use of or inability to use the product, whether or not based on tort (including negligence), strict liability, breach of contract, breach of warranty or any other theory, even if advised of the possibility of such damages. Notwithstanding any damages that customer might incur for any reason whatsoever (including without limitation, all damages referenced above and all direct or general damages), the entire liability of NXP Semiconductors, its affiliates and their suppliers and customer's exclusive remedy for all of the foregoing shall be limited to actual damages incurred by customer based on reasonable reliance up to the greater of the amount actually paid by customer for the product or five dollars (US\$5.00). The foregoing limitations, exclusions and disclaimers shall apply to the maximum extent permitted by applicable law, even if any remedy fails of its essential purpose.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

### 13.3 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

**GreenChip** — is a trademark of NXP B.V.

## Contents

---

<b>1</b>	<b>Introduction .....</b>	<b>3</b>
1.1	Key features .....	4
1.2	Applications .....	4
<b>2</b>	<b>Safety warning .....</b>	<b>5</b>
<b>3</b>	<b>Specifications .....</b>	<b>5</b>
<b>4</b>	<b>Board photographs .....</b>	<b>6</b>
<b>5</b>	<b>TEA1936xDB1463 demo board connections ....</b>	<b>7</b>
<b>6</b>	<b>TEA1936xDB1463 demo board performance .....</b>	<b>8</b>
6.1	Efficiency .....	8
6.2	No-load power consumption at 5 V output .....	9
6.3	ElectroMagnetic Interference (EMI) .....	10
6.4	Thermal .....	11
<b>7</b>	<b>PDO settings .....</b>	<b>12</b>
<b>8</b>	<b>Schematic .....</b>	<b>13</b>
<b>9</b>	<b>Bill Of Materials (BOM) .....</b>	<b>15</b>
<b>10</b>	<b>Layout .....</b>	<b>18</b>
<b>11</b>	<b>Abbreviations .....</b>	<b>20</b>
<b>12</b>	<b>References .....</b>	<b>20</b>
<b>13</b>	<b>Legal information .....</b>	<b>21</b>

---

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

---

© NXP B.V. 2018.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

Date of release: 5 February 2018

Document identifier: UM11058