

i.MX 6SLL Migration Guide

Migrating from i.MX 6SL to i.MX 6SLL

Contents

1 Introduction.....	1
2 Summary.....	1

1 Introduction

1.1 Purpose

This Application Note is intended to be a migration guide for development teams that are porting platforms from i.MX 6SL to i.MX 6SLL.

1.2 Scope

i.MX 6SLL is pin-to-pin compatible with i.MX 6SL, this white paper provide differentiation from i.MX6SL.

i.mx

1.3 Audience

The content of this document is targeted towards system integrators and software developers migrating from platforms based on i.MX 6SL.

2 Summary

Category	Feature	Change from i.MX6SL	Board Change	SW Change
General Connectivity Interfaces	eMMC	Enhance	Update eMMC 4.4 interface to eMMC 5.0 interface.	Yes
	SD/eMMC Port	Removed port 4	Port number change from 4 to 3.	
	Ethernet	Removed	No	No
External Memory	DRAM	Updated	Add support to LPDDR3. Remove support to DDR3/DDR3L.	Yes
	Parallel Nor/EBI	Removed	No	No

Table continues on the next page...



Summary

Table continued from the previous page...

Power	Power consumption	Optimized	Power reduction on both analog & digital.	Yes
	Digital LDOs	Remove digital LDOs <ul style="list-style-type: none"> • VDD_SOC • VDD_ARM • VDD_PU 	- Change VDD_SOC_CAP to VDD_SOC_IN - Change VDD_ARM_CAP to VDD_ARM_IN - Remove VDD_PU_IN and VDD_PU_CAP	Yes
Multimedia	PXP/EPDC	Enhance	No	Yes
	Graphic	Removed	No	
	SiPix Display	Removed	Remove support SPDC.	No

How To Reach Us

Home Page:

nxp.com

Web Support:

nxp.com/support

Information in this document is provided solely to enable system and software implementers to use NXP products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document. NXP reserves the right to make changes without further notice to any products herein.

NXP makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does NXP assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in NXP data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including "typicals," must be validated for each customer application by customer's technical experts. NXP does not convey any license under its patent rights nor the rights of others. NXP sells products pursuant to standard terms and conditions of sale, which can be found at the following address: nxp.com/SalesTermsandConditions.

NXP, the NXP logo, NXP SECURE CONNECTIONS FOR A SMARTER WORLD, ARM, AMBA, ARM Powered, are registered trademarks of ARM Limited (or its subsidiaries) in the EU and/or elsewhere. All rights reserved.

© 2017 NXP B.V.

