



# Up to 100MHz Digital Signal Controllers with DSASS and Operational Amplifier

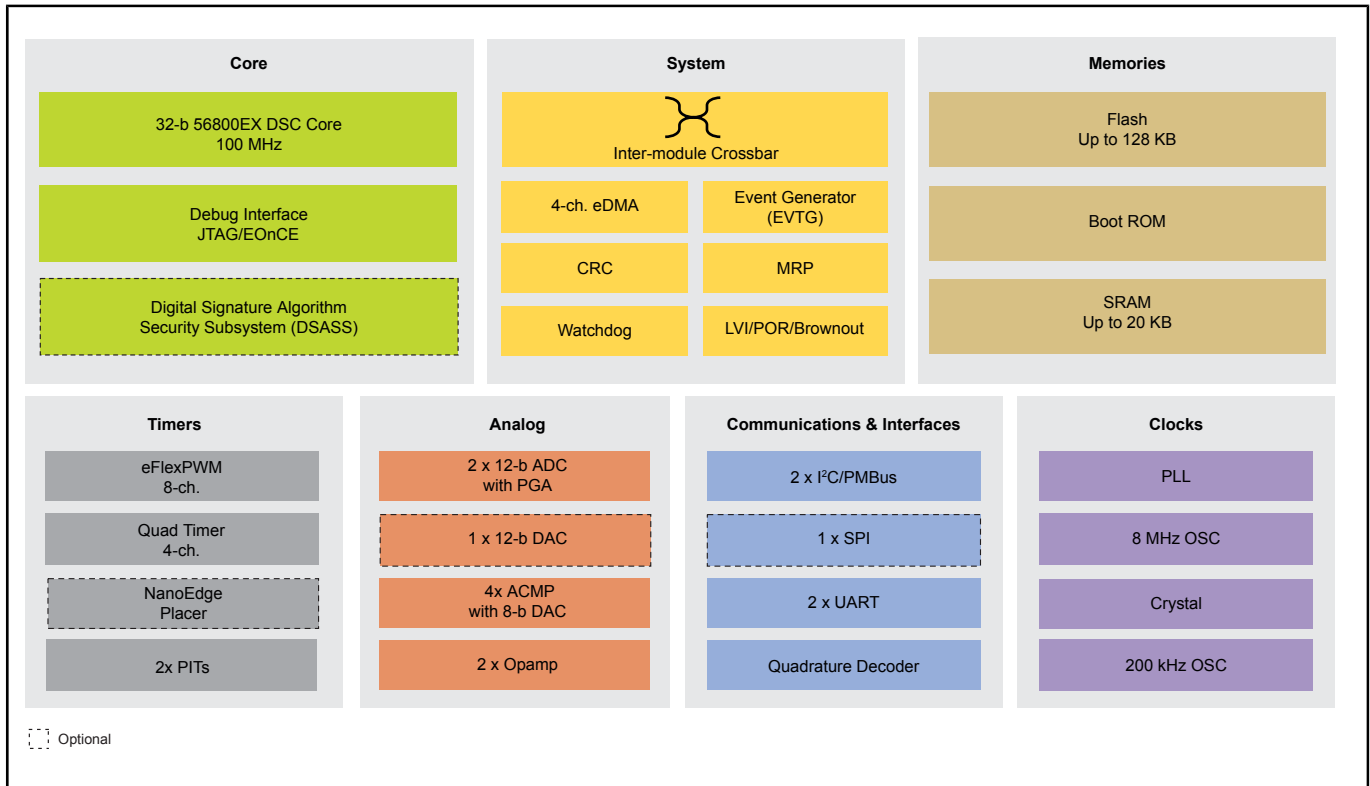
## MC56F81xxx

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The MC56F81xxx family is NXP's new entry-level Digital Signal Controllers (DSC) product. This high efficiency family is based on the high performance 56800EX DSP core, with frequency up to 100MHz. This family extends the DSC products' cost performance to the best, it's NXP's first DSC product to integrate a Digital Signature Algorithm Security Subsystem (DSASS) and high-speed low-power operational amplifier. Comparing with the MC56F82xxx family, the MC56F81xxx family extends the memory to 128KB Flash, 20KB RAM and 64KB boot ROM. In addition, MC56F81xxx family has lots of feature enhancements to the MC56F82xxx family include enhanced DMA function (eDMA), configurable logic Event Generator (EVTG), dual 1.6MSps 12-bit ADC and full PMBus support. The MC56F81xxx family offers extremely cost-effective solution for power conversion and motor control applications.

For additional information and sample availability, contact your local [NXP® Sales Office](#) or [NXP Authorized Distributor](#).

# 56F81xxx Block Diagram



View additional information for [Up to 100MHz Digital Signal Controllers with DSASS and Operational Amplifier](#).

**Note:** The information on this document is subject to change without notice.

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