



# Layerscape® 1088A and 1048A Processor

## LS1088A

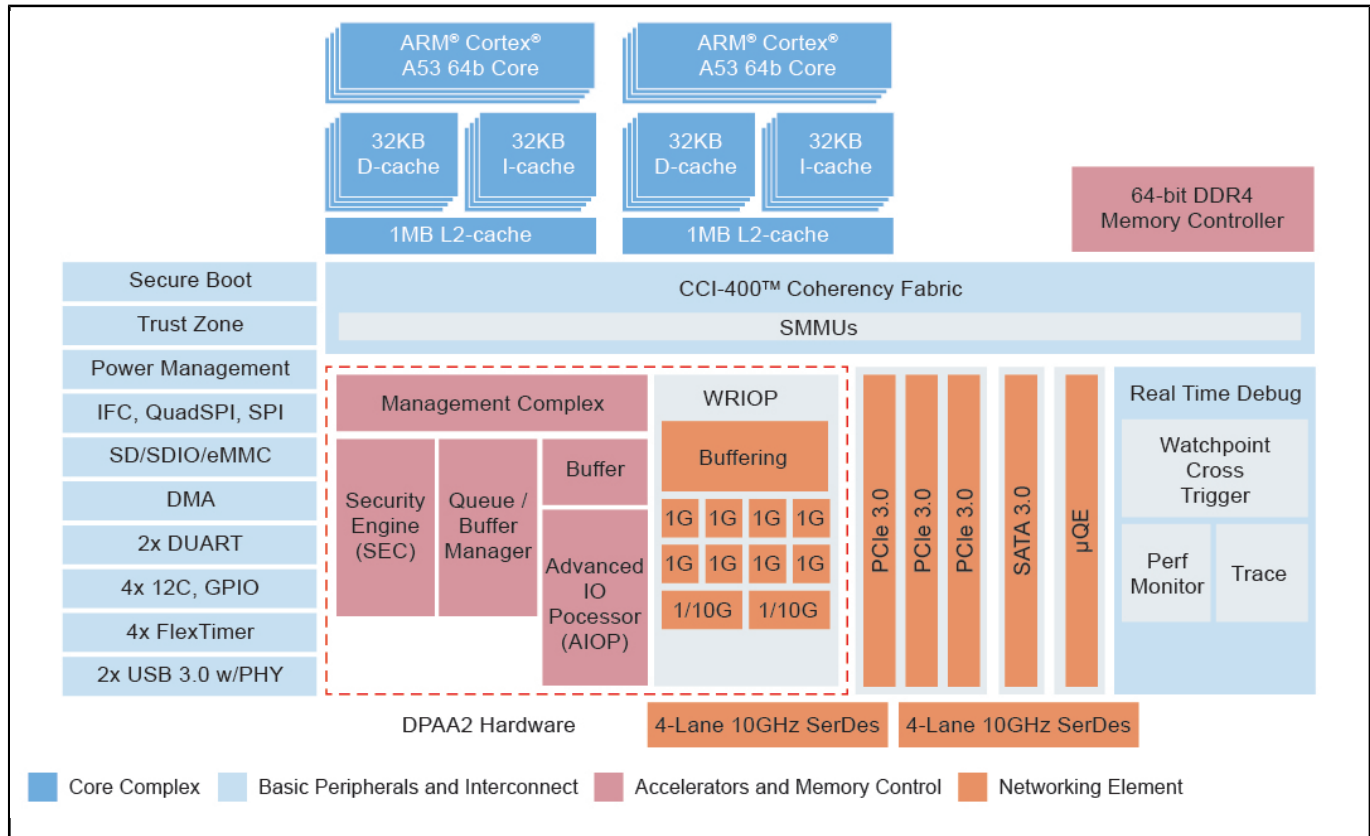
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The LS10x8A family of multicore communications processors combines four up to and eight Arm® Cortex®-A53 cores with the advanced, high-performance data path and network peripheral interfaces required for wireless access points, networking infrastructure, intelligent edge access, including virtual customer premise equipment (vCPE) and high-performance industrial applications.

The Arm® general-purpose processors and DPAA2 are supported by a powerful software toolkit that provides a higher level of hardware abstraction and makes software development quick and simple. This combination balances ease-of-use with high-performance processing in a Linux® environment that is familiar to any software programmer.

Layerscape processors are part of NXP's EdgeVerse™ [edge computing](#) platform.

# LS1088A Block Diagram Block Diagram



View additional information for [Layerscape® 1088A and 1048A Processor](#).

Note: The information on this document is subject to change without notice.

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