

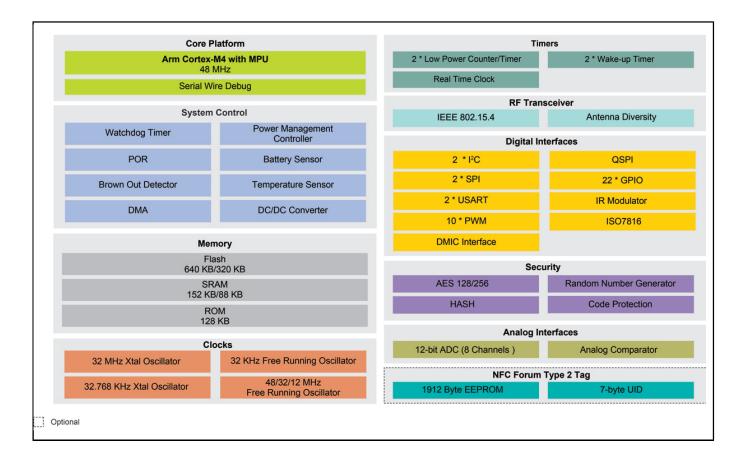
JN5189/88 (T): High-Performance and Ultra-Low-Power MCUs for Zigbee® and Thread with Built-In NFC Option

JN5189_88_T

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The JN5189 portfolio is designed to power the next generation of very low current wireless devices, supporting Zigbee 3.0, Thread, and IEEE 802.15.4. It includes several low-power modes and ultra-low TX and RX power consumption, which enables devices powered by JN5189/88 to have a longer battery life. With -100 dBm RX sensitivity and up to +11 dBm TX output power, JN5189/88 offers reliable and robust communications performance.JN5189/88 is powered by an Arm® Cortex®-M4 MCU and can run up to 640 KB onboard flash and 152 KB SRAM, with enough room and flexibility for complex applications and OTA upgrade capability without external memory. It has a rich set of MCU peripherals and multiple serial communication interfaces for embedded connected applications. JN5189T/88T has an integrated NFC NTAG to implement contactless NFC commissioning, simplifying the network build-out.

JN5189/88 Block Diagram Block Diagram



View additional information for JN5189/88 (T): High-Performance and Ultra-Low-Power MCUs for Zigbee® and Thread with Built-In NFC Option.

Note: The information on this document is subject to change without notice.

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