



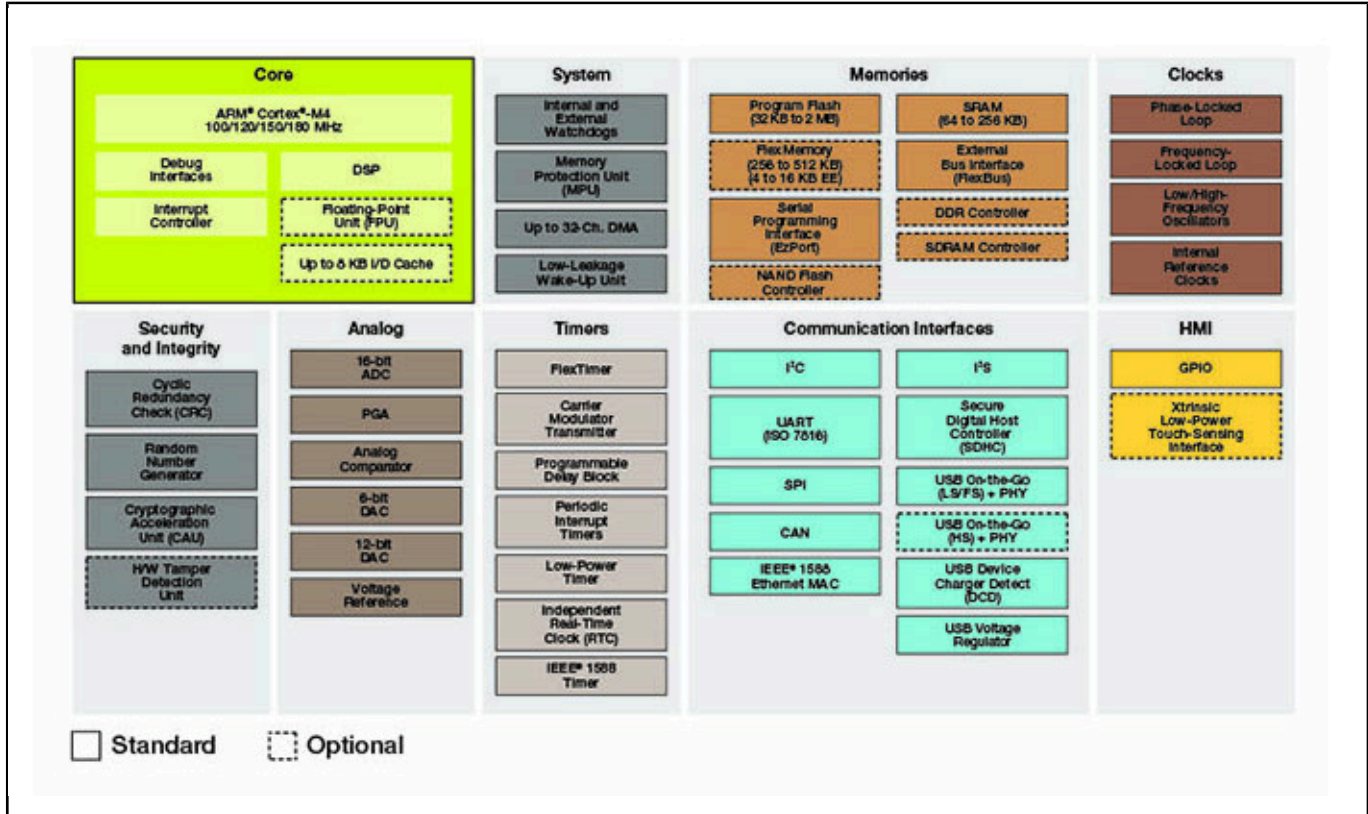
# Kinetis® K60-100 MHz, Mixed-Signal Integration Microcontrollers based on Arm® Cortex®-M4 Core

## K60\_100

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The Kinetis® K60 100 MHz IEEE® 1588 Ethernet MCUs are built on the Arm® Cortex® -M4 core and feature advanced analog integration and serial communication. Available down to a 5 mm x 5 mm wafer-level chip-scale package (WLCSP), these devices maximize board space and enhance performance with minimum-length interconnections, allowing the miniaturization of existing applications. This family shares the comprehensive enablement and scalability of the Kinetis portfolio.

# Kinetis K6x MCU Family Block Diagram Block Diagram



View additional information for [Kinetis® K60-100 MHz, Mixed-Signal Integration Microcontrollers based on Arm® Cortex®-M4 Core](#).

Note: The information on this document is subject to change without notice.

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