

AN12398

EdgeLock SE05x Quick start guide with Visual Studio project examples

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534625

Application note

Document information

Information	Content
Keywords	EdgeLock SE05x, EdgeLock SE05x Plug & Trust Middleware, Visual Studio projects
Abstract	This document explains how to get started with EdgeLock SE05x Plug & Trust middleware using the Visual Studio project examples. It provides detailed instructions to run the Microsoft Visual Studio projects using the CMake-based build system included in the EdgeLock SE05x Plug & Trust middleware.



Revision history

Revision history

Revision number	Date	Description
1.0	2019-06-08	First document release
1.1	2019-06-20	Update of board figures
2.0	2019-11-25	Update of CMake build system materials
2.1	2019-12-17	Corrected OM-SE05xARD J14 jumper setting
2.2	2020-11-19	Update for EdgeLock SE051
2.3	2020-12-07	Updated to the latest template and fixed broken URLs
2.4	2022-06-02	<p>Add EdgeLock SE050E and EdgeLock A5000 product variants.</p> <p>Update Table 1, Figure 1, Figure 2, Figure 3, Figure 5, Figure 6, Figure 7, Figure 8, Figure 9, Figure 10, Figure 11, Figure 12, Figure 13, Figure 14, Figure 15, Figure 16, Figure 17, Figure 18, Figure 19, Figure 20, Figure 21, Figure 22, Figure 23, Figure 31, Figure 32, Figure 33, Figure 34, Figure 35, Figure 36, Figure 43, Figure 44 and Figure 45.</p> <p>Add Section Section 5 Product specific CMake build settings.</p> <p>Add Section Binding EdgeLock SE05x to a host using Platform SCP Binding EdgeLock SE05x to a host using Platform SCP.</p> <p>Update tool versions in Section 7, Section 8 and Section 9.</p> <p>Moved section "Update FRDM-K64F board with DAPLink firmware" into Section 10.</p>
2.5	2022-07-04	<p>Update to EdgeLock SE Plug & Trust Middleware version 04.02.xx.</p> <p>Update Section Section 5 Product specific CMake build settings.</p> <p>Update Section Binding EdgeLock SE05x to a host using Platform SCP Binding EdgeLock SE05x to a host using Platform SCP.</p>

1 How to use this document

The Plug & Trust middleware package is delivered with the CMake files that include the set of directives and instructions describing the project's source files and targets. The CMake architecture allows developers to build files for their platform and native build environment and run exactly the same project example on PC/Windows/Linux and embedded targets.

This document provides detailed instructions to run Visual Studio examples provided in the Plug & Trust middleware using FRDM-K64F and OM-SE05xARD boards. The main body of this document should be used in this sequence:

1. Order board samples. [Section 2](#) contains the ordering details of the boards required in this document
2. Setup your boards. [Section 3](#) describes how to setup the OM-SE05xARD and FRDM-K64F boards.
3. Run project examples. Go to [Section 4](#) for instructions to import and run EdgeLock SE05x Visual Studio project examples.

Supplementary material has been provided in the appendices.




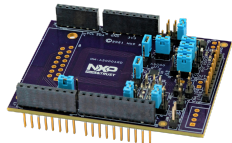
2 Hardware required

The EdgeLock SE05x works as an auxiliary security device attached to a host controller, communicating with through an I²C interface. To follow the instructions provided in this document, you need an EdgeLock SE05x development board and a FRDM-K64F MCU board, acting as a host controller.

EdgeLock SE05x development boards ordering details

The EdgeLock SE05x and EdgeLock A5000 product support packages are providing development boards for evaluating EdgeLock SE05x and EdgeLock A5000 features. Select the development board of the product you want to evaluate. [Table 1](#) details the ordering details of the EdgeLock SE05x and EdgeLock A5000 development boards.

Table 1. EdgeLock SE05x development boards.

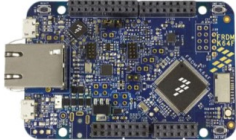
Part number	12NC	Description	Picture
OM-SE050ARD-E	9354 332 66598	SE050E Arduino® compatible development kit	
OM-SE050ARD-F	9354 357 63598	SE050 Arduino® compatible development kit	
OM-SE050ARD	9353 832 82598	SE050F Arduino® compatible development kit	
OM-SE051ARD	9353 991 87598	SE051 Arduino® compatible development kit	
OM-A5000ARD	9354 243 19598	A5000 Arduino® compatible development kit	

Note: The pictures in this guide will show SE050E, but all boards in [Table 1](#) can be used as well with the same hardware configuration.

FRDM-K64F MCU board ordering details

[Table 2](#) details the ordering details for the FRDM-K64F board.

Table 2. FRDM-K64F details

Part number	12NC	Content	Picture
FRDM-K64F	935326293598	Freedom development platform for Kinetis K64, K63 and K24 MCUs	

3 Boards setup

This section explains how to prepare the OM-SE05xARD boards and FRDM-K64F board to run the Plug & Trust middleware project examples. This consists of:

1. [OM-SE05xARD jumper configuration](#).
2. [OM-SE05xARD and FRDM-K64F board connection](#).

Note: If your FRDM-K64F board does not already contain the DAPLink firmware, you need to update the FRDM-K64F board as described in [Section 10](#).

3.1 OM-SE05xARD jumper configuration

The OM-SE05xARD boards have jumpers that allow you to configure the I²C interface of EdgeLock SE05x secure elements via the Arduino header. Configure the jumper settings as shown in [Figure 1](#) to enable this option.

Note: For more information about the jumper settings, refer to [AN13539 OM-SE05xARD hardware overview](#).

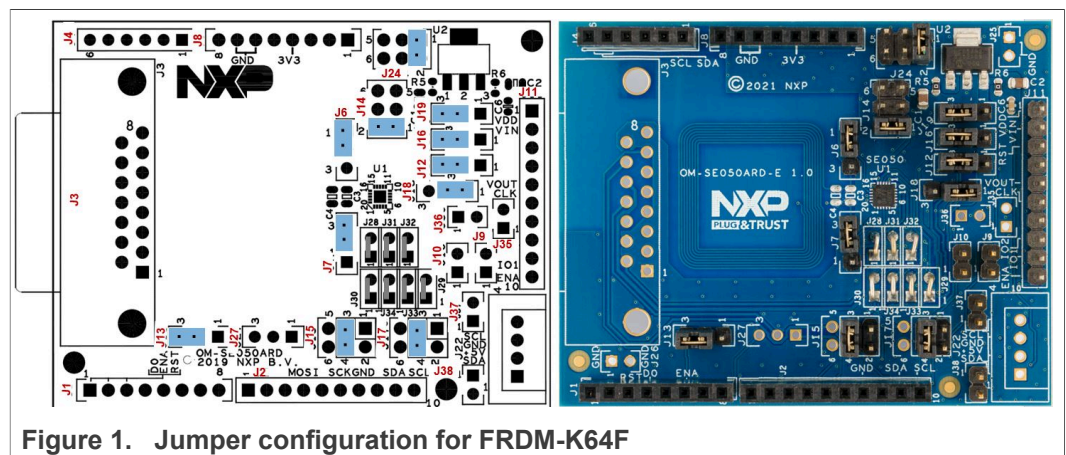
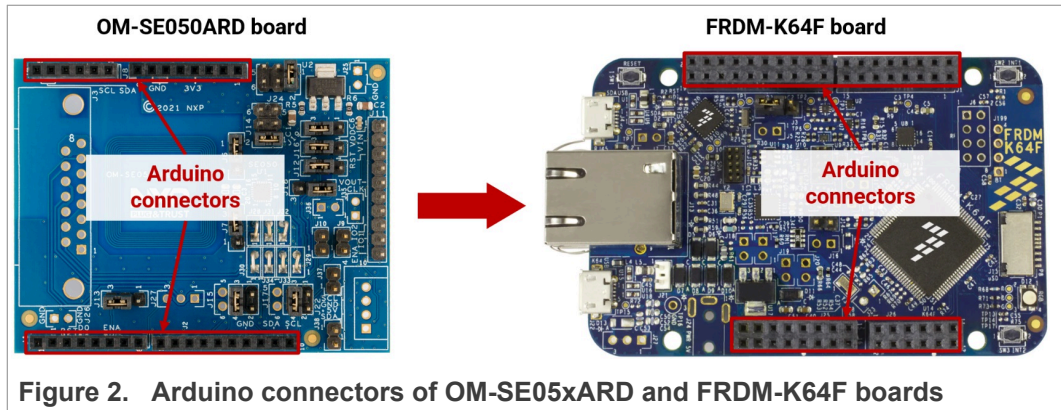


Figure 1. Jumper configuration for FRDM-K64F

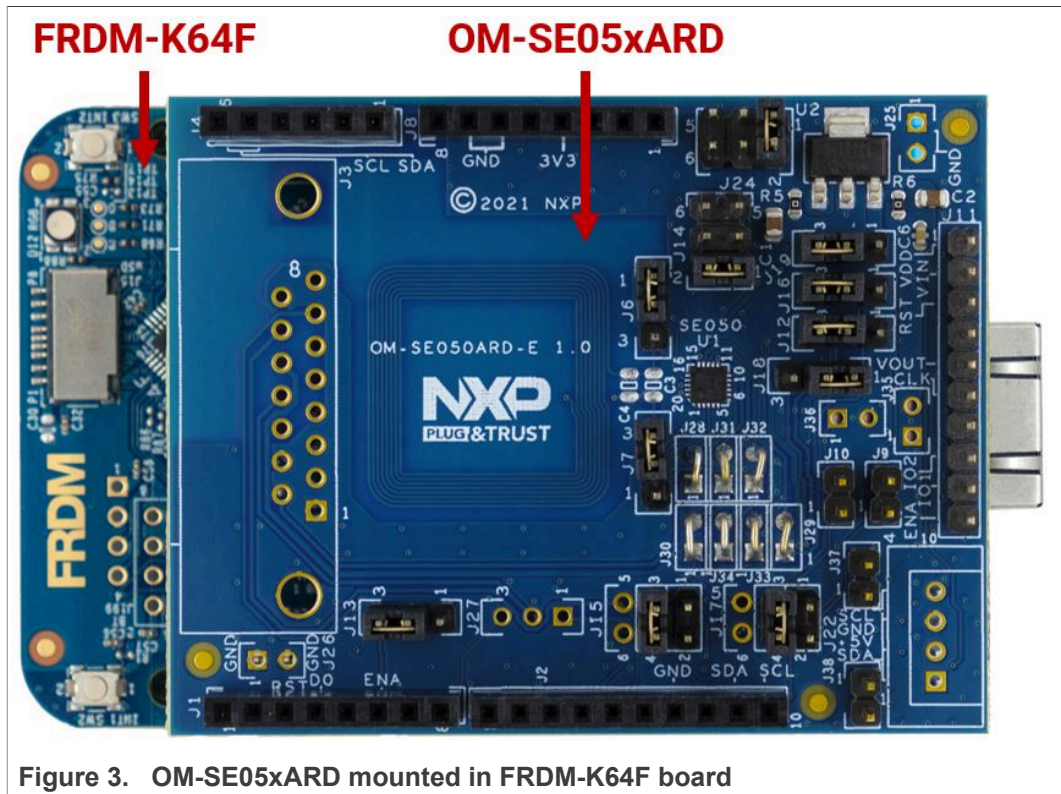
3.2 OM-SE05xARD and FRDM-K64F board connection

The OM-SE05xARD boards and FRDM-K64F board can be directly connected using the Arduino connectors. The OM-SE05xARD boards come with male connectors while the FRDM-K64F board comes with female headers.

Mount any OM-SE05xARD board on top of the FRDM-K64F as shown in [Figure 2](#):



Double check that the two boards are connected as shown in [Figure 3](#):

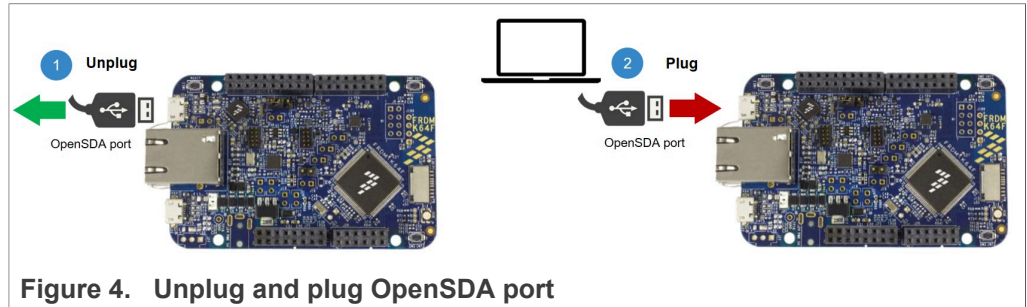


Note: Refer to [Figure 1](#) for OM-SE05xARD jumper configuration.

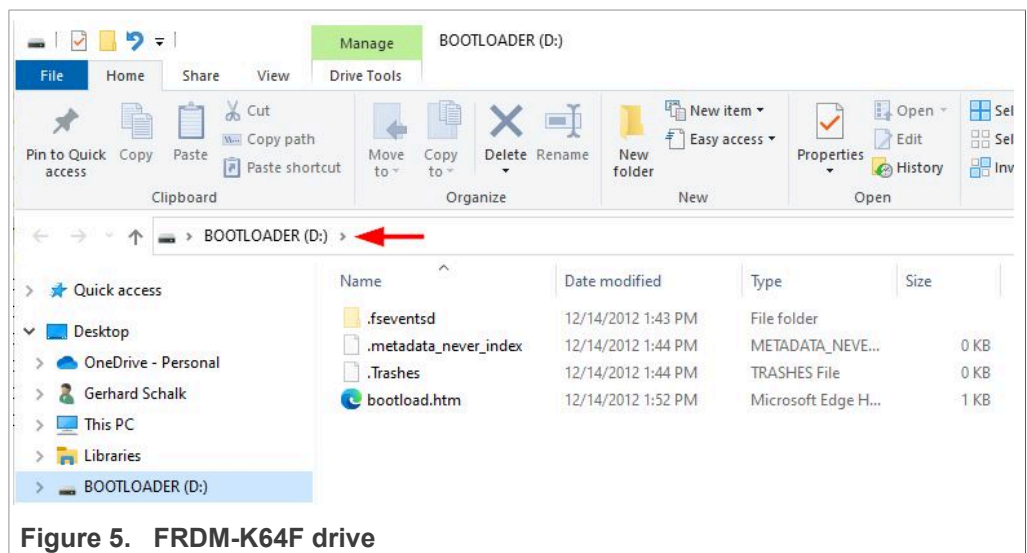
3.3 Flash FRDM-K64F with VCOM software

The VCOM software allows the FRDM-K64F board to be used as a bridge between the Windows machine and the EdgeLock SE05x and enables the execution of the EdgeLock SE05x `sscli` tool and other utilities from the laptop. To flash the VCOM software into the FRDM-K64F, follow these steps:

1. Unplug and plug again the USB cable to the openSDA USB port as shown in [Figure 4](#):

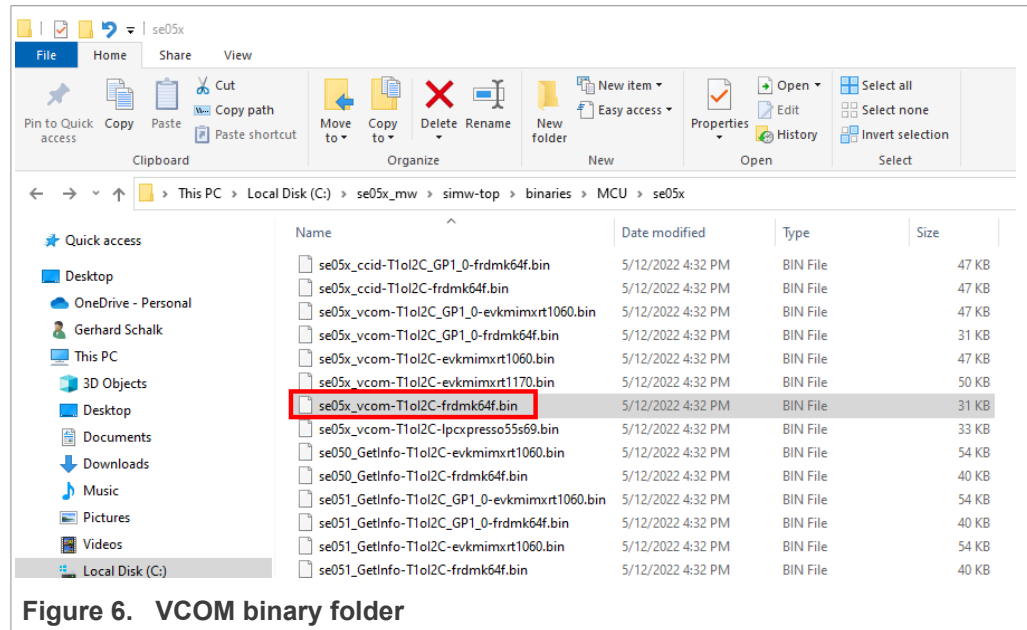


2. When you plug the board, your laptop should recognize the board as an external drive as shown in [Figure 5](#):

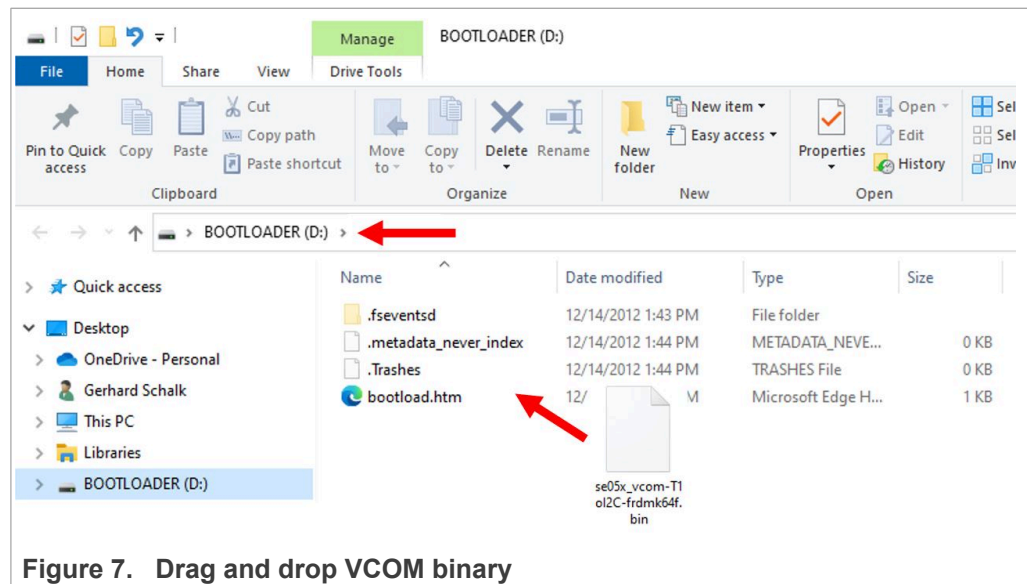


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3. Flash the VCOM software to FRDM-K64F. The VCOM software binary can be found in the Plug & Trust middleware package, inside the `simw-top\binaries` folder as shown in [Figure 6](#):



4. Drag and drop or copy and paste the `a7x_vcom-T1oI2C-frdmk64f-SE050x.bin` file into the FRDM-K64F drive from your computer file explorer as shown in [Figure 7](#):



5. The serial and VCOM ports should be recognized by your Device Manager. To check that the ports are recognized, follow the steps indicated in [Figure 8](#):
 - a. Unplug the USB cable from the OpenSDA USB port.
 - b. Plug the USB cable to the OpenSDA USB port.
 - c. Check that the serial port is recognized in the category **Ports (COM & LTP)**. In this document, it is recognized as **USB Serial Device (COM7)** but this naming

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might change depending on your computer. Therefore, it is important that you identify which device is recognized at the moment you plug the SDA USB port to the computer.

- d. Plug the USB cable to the K64F USB port.
- e. Check that the VCOM port is recognized in the category **Ports (COM & LPT)**. In this document, it is recognized as *Virtual Com Port (COM8)* but this naming might change depending on your computer (e.g. It could also appear named as *USB Serial Device*). Therefore, it is important that you identify which device is recognized at the moment you plug the K64F USB port to the computer.

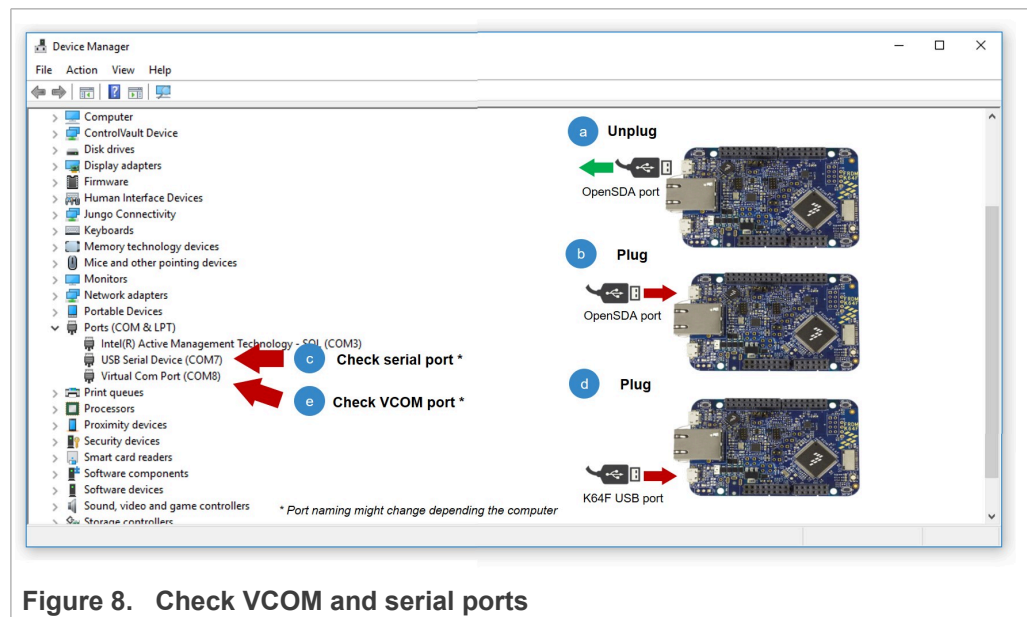


Figure 8. Check VCOM and serial ports

4 Run Plug & Trust middleware Visual Studio project examples

This section explains how to run Plug & Trust middleware Visual Studio project examples using the CMake-based build system.

4.1 Prerequisites

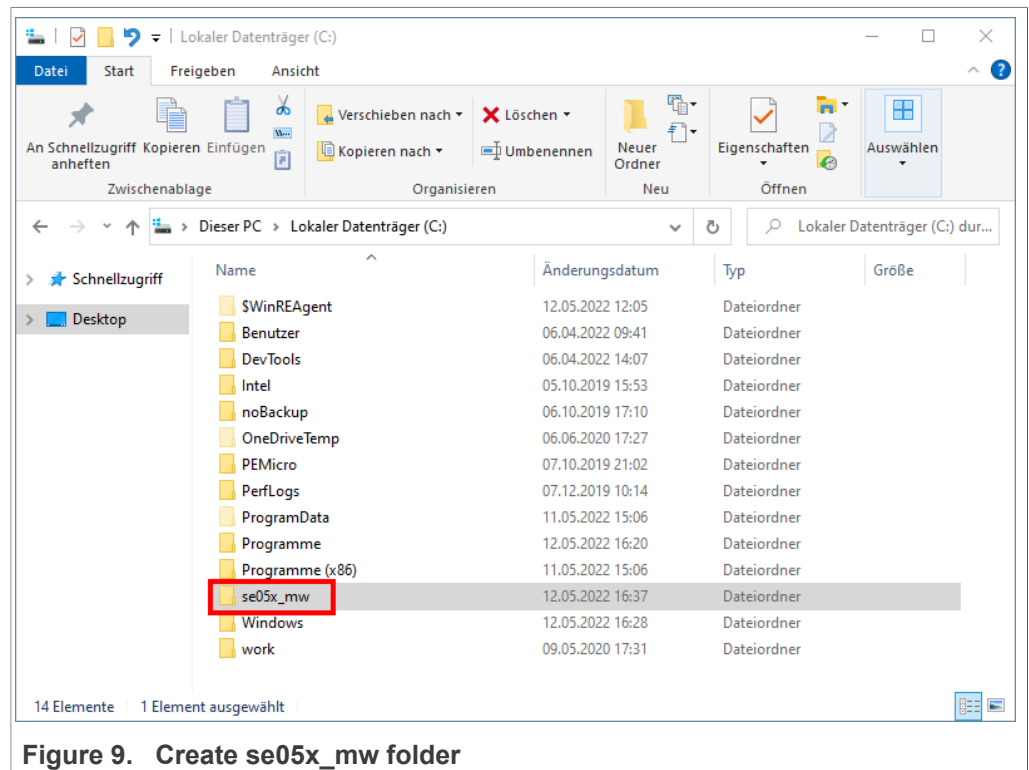
The following tools are required to run the Plug & Trust middleware Visual Studio projects:

1. Install Visual Studio ≥ 2017 version, or higher, in your laptop. For reference, [Section 7](#) illustrates how Visual Studio 2022 version can be installed, but the same procedure can be applied for more recent versions.
2. Install CMake. For reference, [Section 8](#) illustrates the detailed installation instructions.
3. Install Python Python ≥ 3.7.x and ≤ 3.9.x 32-bit version, in your laptop. For reference, [Section 9](#) illustrates how Python 3.7.x 32-bit version can be installed, but the same procedure can be applied for more recent versions.

4.2 Download Plug & Trust middleware

Follow these steps to download the Plug & Trust middleware in your local machine:

1. Download Plug & Trust middleware from the [NXP website](#).
2. Create a folder called **se05x_mw** in C: directory as shown in [Figure 9](#):



- Unzip the Plug & Trust middleware inside the `se05x_mw` folder. After unzipping, you will see a folder called **simw-top** created. The contents of the **simw-top** directory should look as shown in [Figure 10](#):

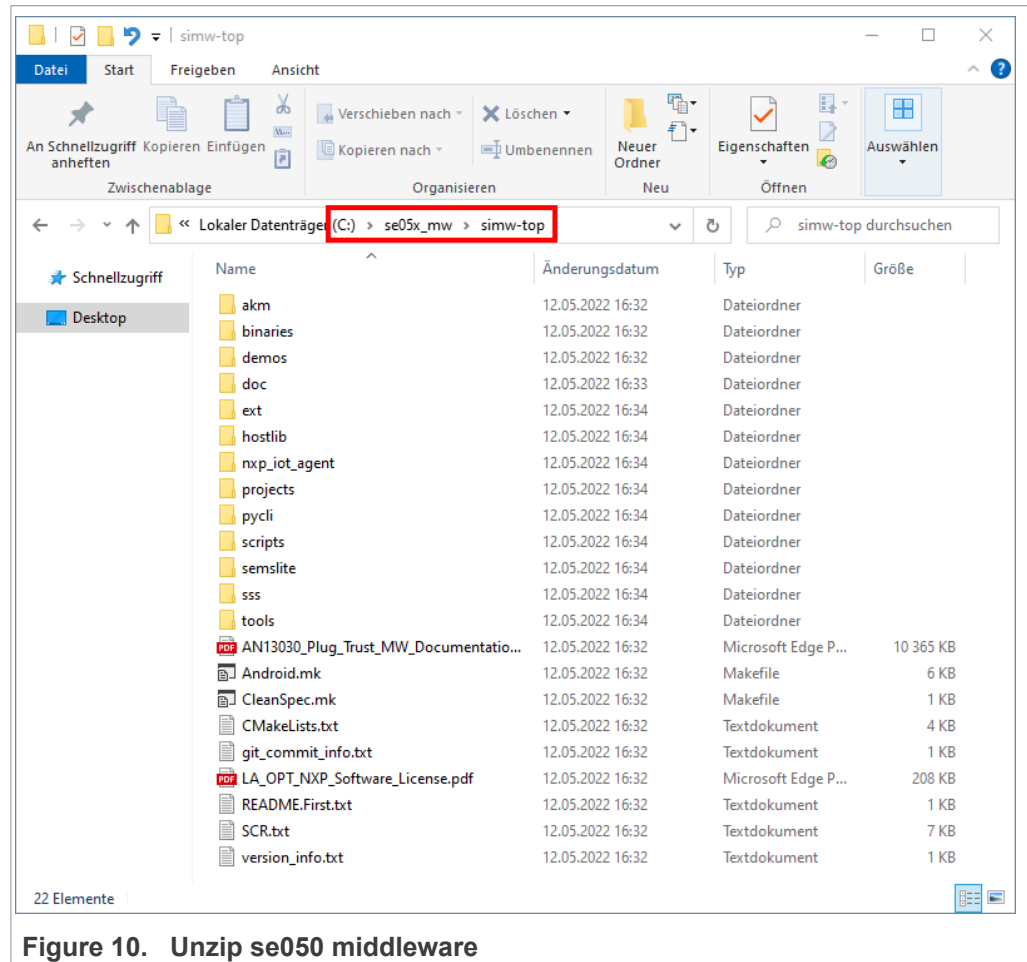


Figure 10. Unzip se050 middleware

Note: It is recommended to keep `se05x_mw` with the **shortest** path possible and **without spaces** in it. This avoids some issues that could appear when building the middleware if the path contains spaces.

4.3 Build Plug & Trust middleware project examples

The Plug & Trust middleware uses CMake for building the project examples into your local machine. To build Plug & Trust middleware, open a Command Prompt and use the following steps as shown in [Figure 12](#):

- Go to folder with the unzipped SE050 middleware:
 (1) Send `>> cd C:\se05x_mw\simw-top\scripts`

2. Define the environment:
 - (2) Send >> env_setup.bat

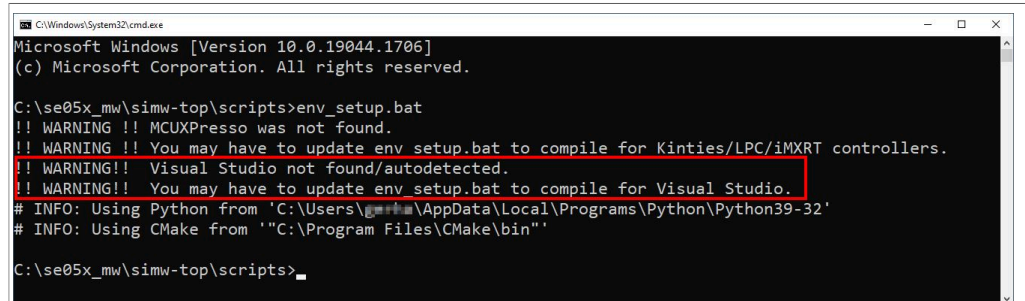


Figure 11. Generate Plug & Trust middleware define the environment

Depending on your PC installation you may need to update the application file locations within the env_setup.bat file.

3. Generate the Plug & Trust middleware project examples:
 - (3) Send >> python create_cmake_projects.py

Note: This command may take a few seconds to complete.

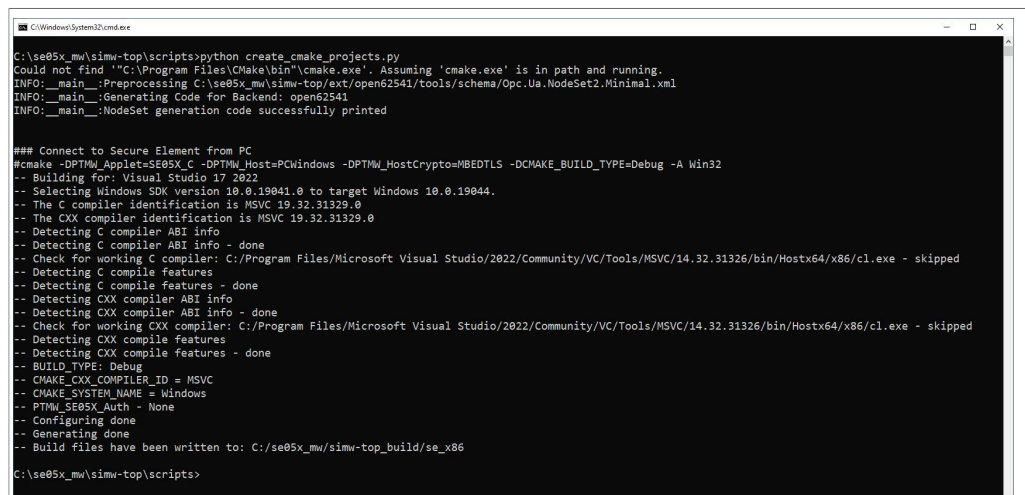
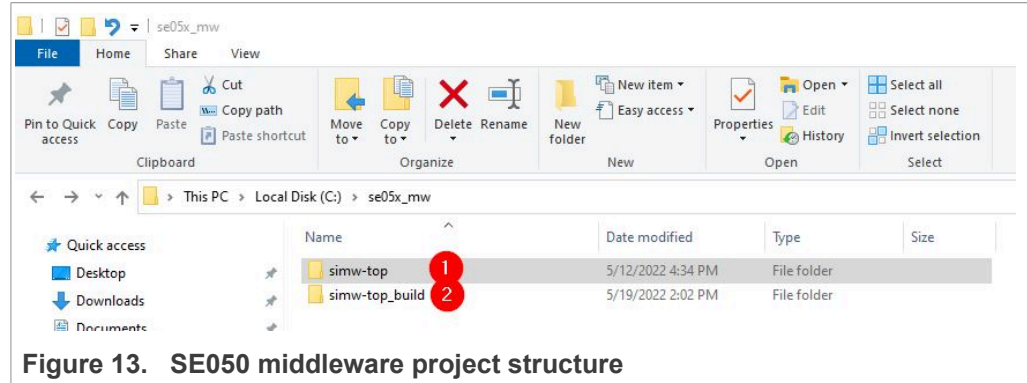


Figure 12. Generate Plug & Trust middleware project examples

- Your project directory should now contain two folders: a (1) `simw-top` folder and a (2) `simw-top_build` folder as shown in [Figure 13](#):



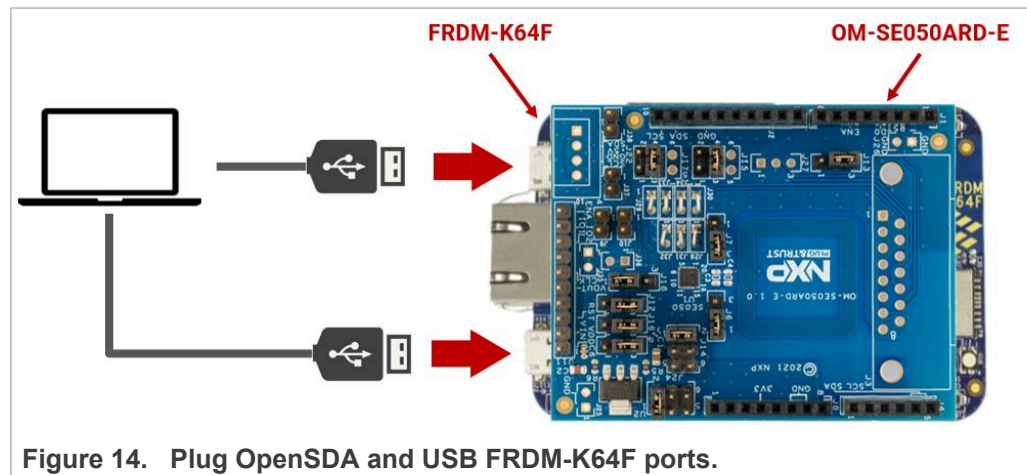
4.4 Execute EdgeLock SE05x Visual Studio project examples

This section explains how to run the Plug & Trust middleware project example called `se05x_minimal`. The `se05x_minimal` project outputs the memory left in EdgeLock SE05x security IC.

Note: The execution of the `se05x_minimal` project is shown as an example. The steps detailed in this section can be replicated to run any other project example included as part of the Plug & Trust middleware.

To execute the `se05x_minimal` test example, follow these steps:

- Connect the FRDM-K64F board to your laptop as shown in [Figure 14](#). Check that your Windows Device Manager recognizes FRDM-K64F board as shown in [Figure 8](#). Refer to [Figure 1](#) for OM-SE05xARD jumper configuration.



2. Open the CMake configuration menu as shown in [Figure 15](#) and ensure that all the flags are set properly for your use case.
 - a. Open a command prompt and go to the directory where the Plug & Trust middleware is built.
Send: `cd C:\se05x_mw\simw-top_build\se_x86`
 - b. Open the cmake configuration interface.
Send: `cmake-gui .`

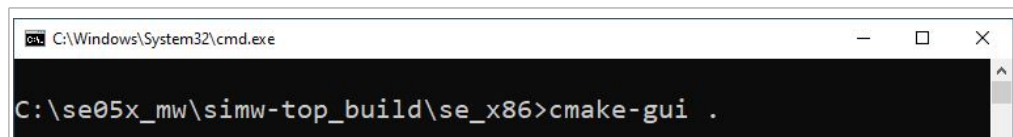


Figure 15. Open the CMake configuration interface

This step allows the user to customize the compilation options. The default build configuration of the EdgeLock SE05x Plug & Trust middleware $\geq v04.02.0x$ generates code for the OM-SE050ARD-E development board. You need to adapt the CMake settings in case you are using a different EdgeLock secure element

development board or a different secure element product IC. The settings are described in [Section 5](#). The default SE050E settings are shown in [Figure 16](#). In this example we use plain communication. Plain communication for the example execution is enabled by selecting the following options:

- Select `None` for the CMake option `PTMW_SE05X_Auth`.
- Select `None` for the CMake option `PTMW_SCP`.

How to enable Platform SCP is described in [How to enable Platform SCP](#).

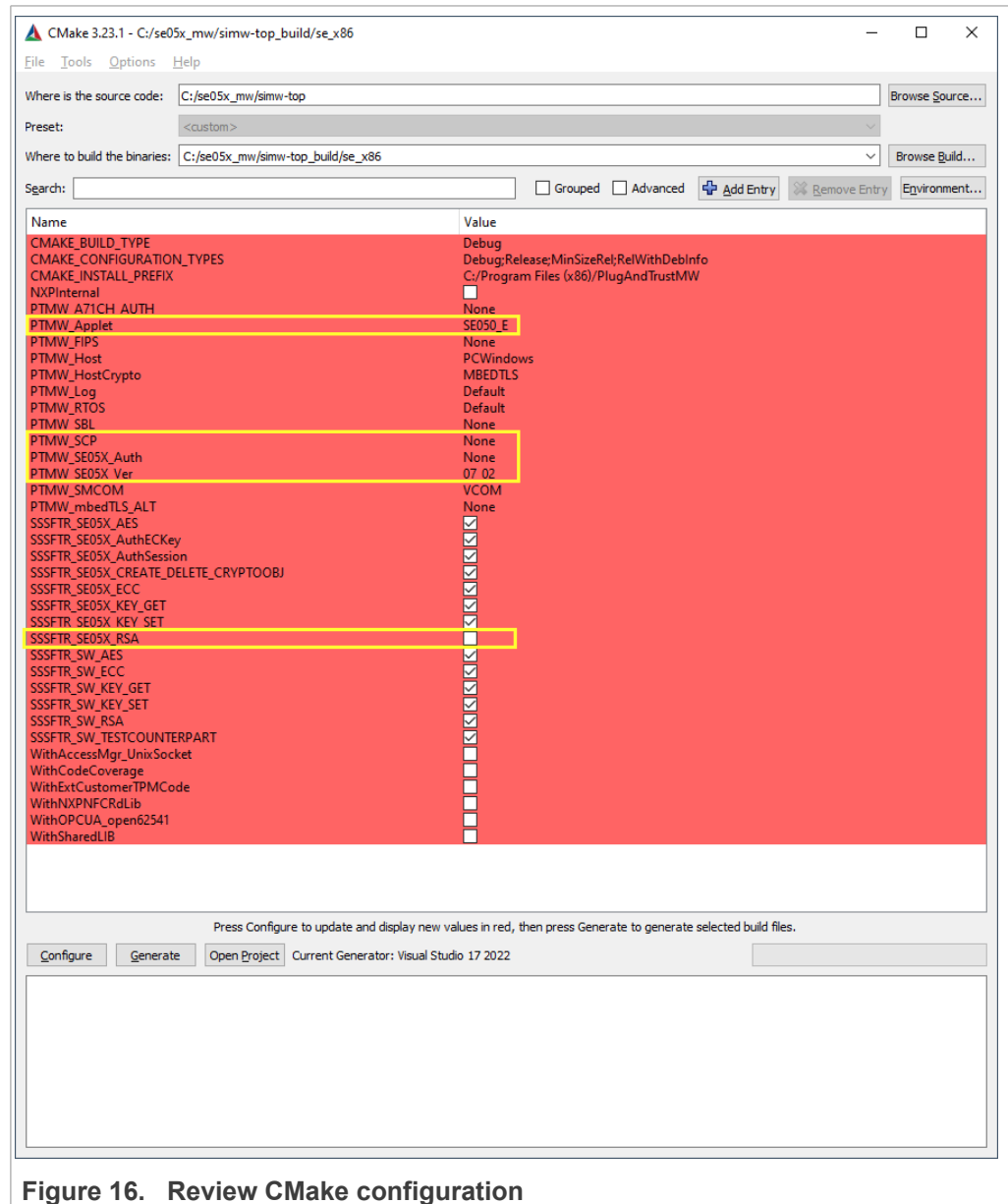


Figure 16. Review CMake configuration

If you have edited any of the parameters in the menu, before exiting press the buttons **Configure** and **Generate** to apply the changes.

For more information about the CMake options please refer the CMake section of Plug & Trust middleware documentation: `simw-top/doc/scripts/cmake_options.html`

- Go to the `C:\se05x_mw\simw-top_build\se_x86` directory. Double click the **PlugAndTrustMW.sln** Visual Studio project solution as shown in [Figure 17](#):

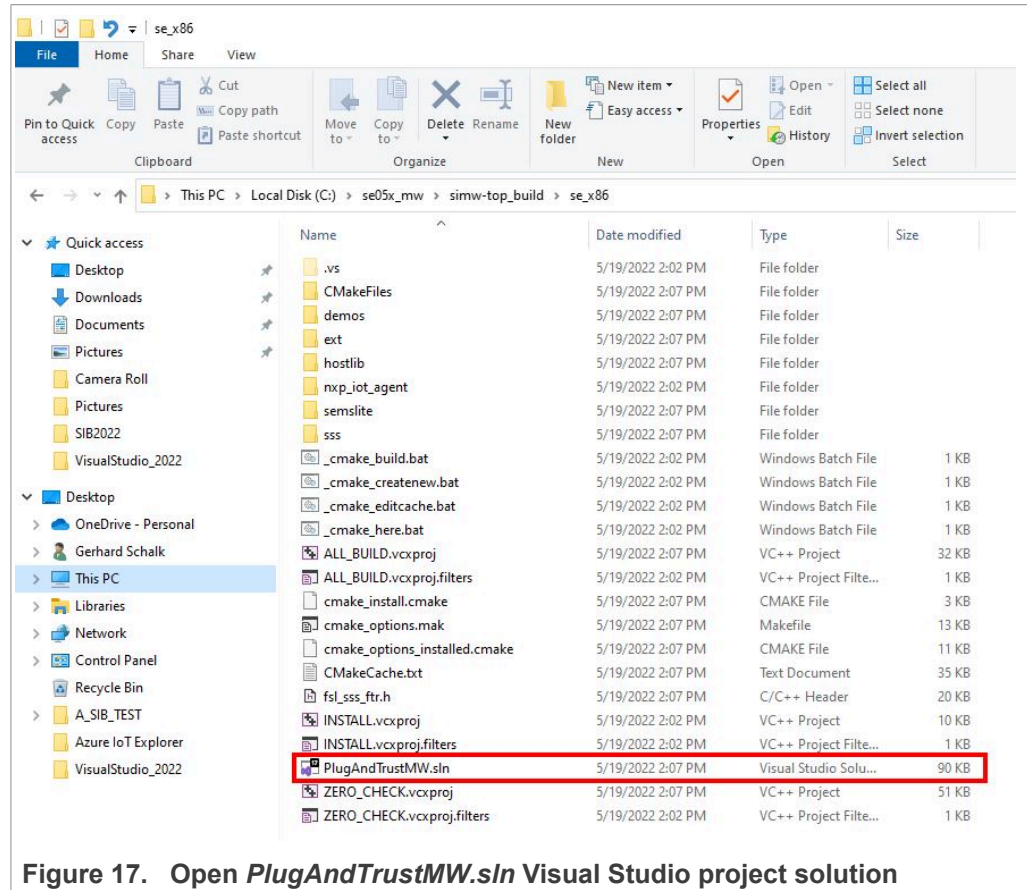


Figure 17. Open *PlugAndTrustMW.sln* Visual Studio project solution

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4. The Visual Studio IDE will open with the Plug & Trust middleware project examples included in the workspace as can be seen in [Figure 18](#):

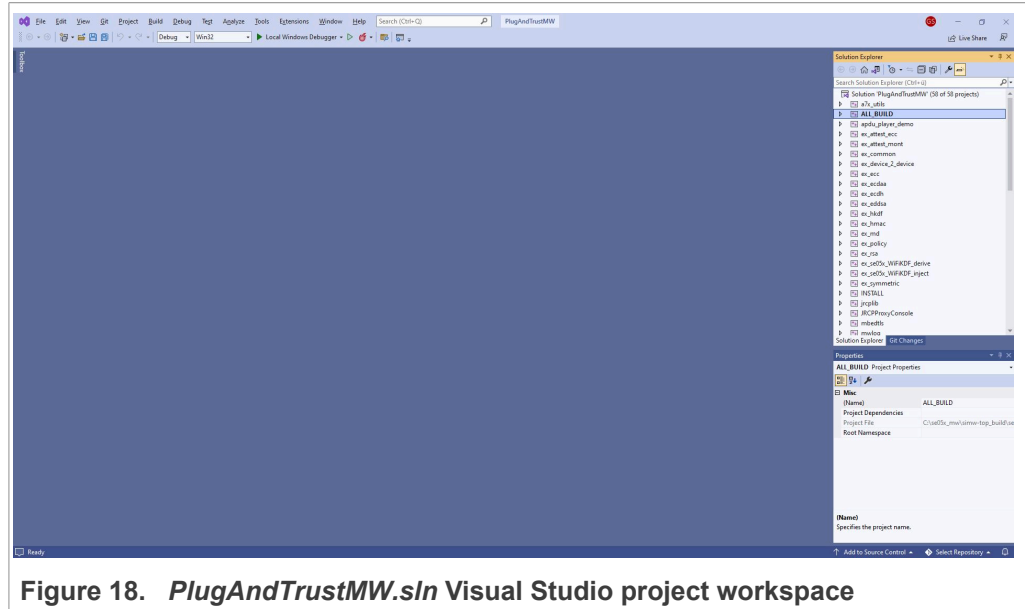


Figure 18. PlugAndTrustMW.sln Visual Studio project workspace

5. Change the VCOM port number in the Plug & Trust middleware project. To do so, follow the instructions shown in [Figure 19](#):
 - a. Go to the ex_common project and open the ex_sss_ports.h file inside the headers directory.
 - b. Change #define EX_SSS_BOOT_SSS_COMPORT_DEFAULT "\\\\.\\COMx" with the port COM number your laptop assigned to your FRDM-K64F. In this setup, the COM number is COM9.

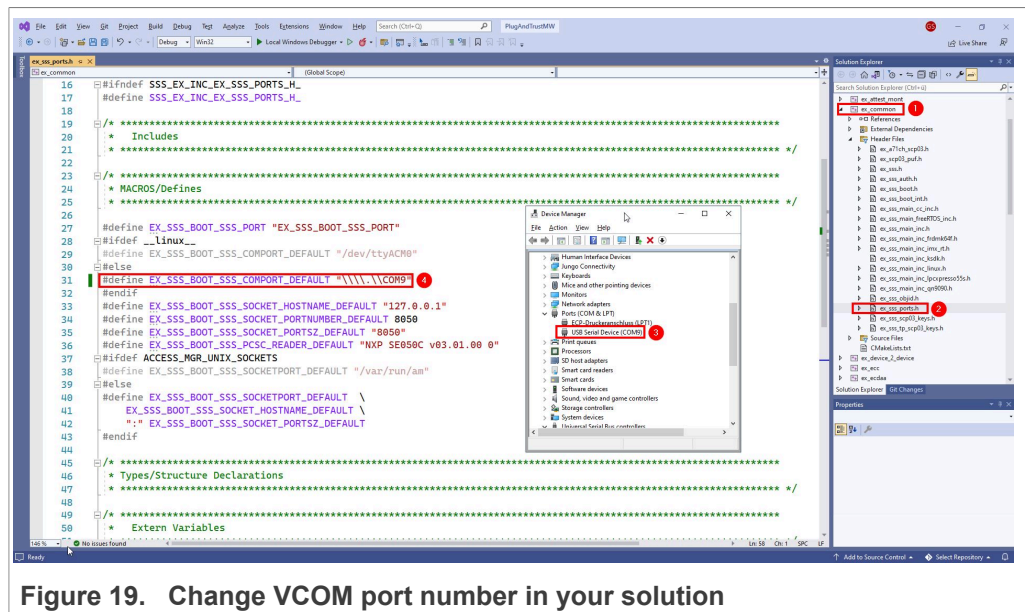


Figure 19. Change VCOM port number in your solution

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- 6. Select the se05x_minimal project from the Solution Explorer window located on the right-hand side of the Visual Studio IDE. Do right-click on the project and click on **Set as Startup project** as shown in [Figure 20](#):

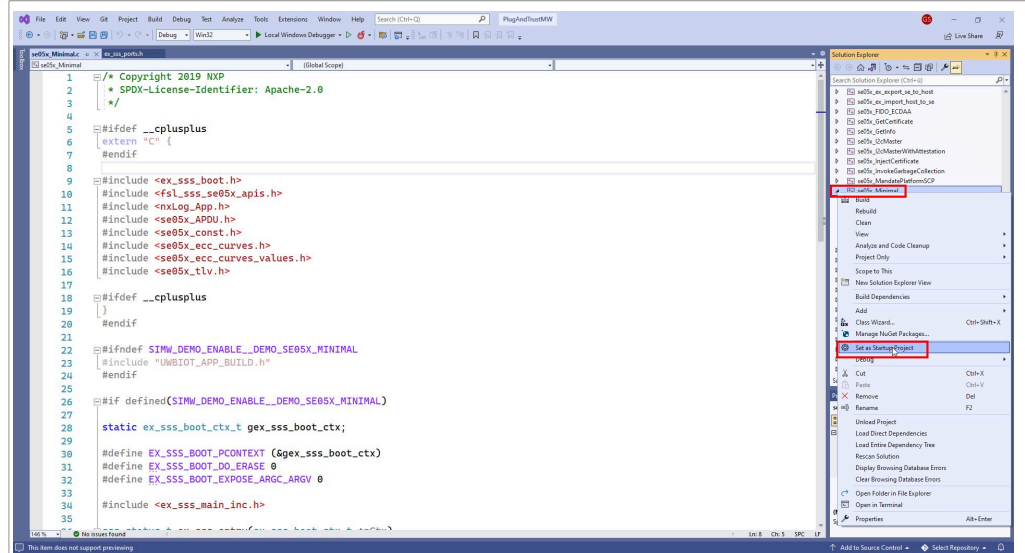


Figure 20. Set se05x_minimal as StartUp project

- 7. Right click on the se05x_minimal project and build it by clicking the build option as shown in [Figure 21](#).

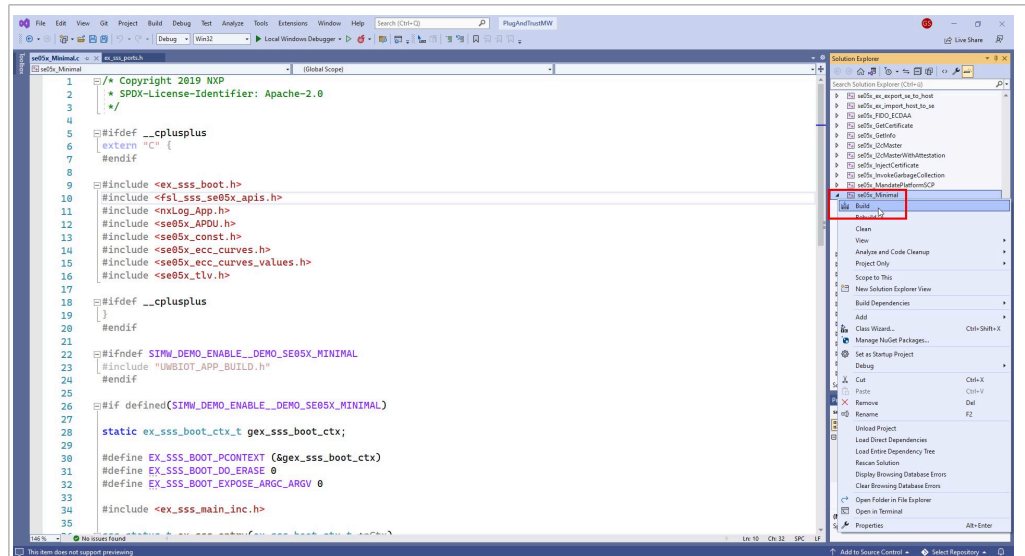


Figure 21. Build se05x_minimal project

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- Click on **Local Windows Debugger** button in the top menu to run `se05x_minimal` project as shown in [Figure 22](#). The project will be executed after the project building process has finished.

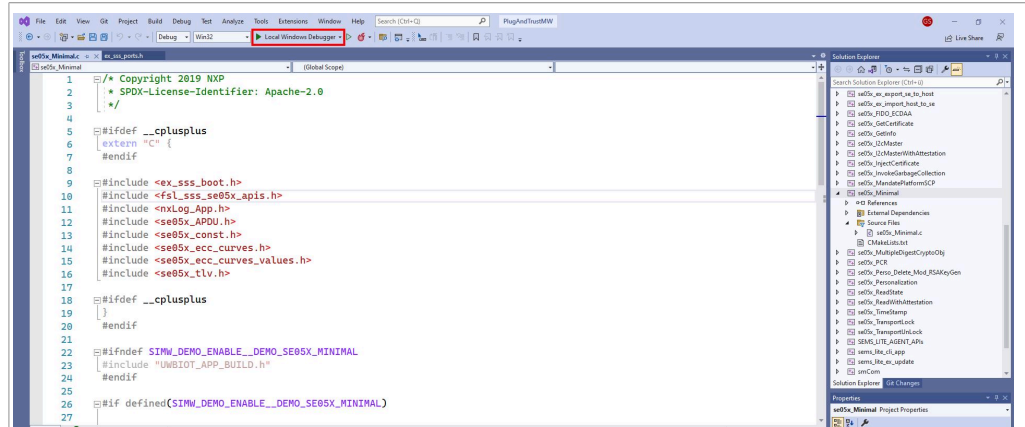


Figure 22. Run `se05x_minimal` project

- If the `se05x_minimal` project runs successfully, a Console window will be opened. The logs in this Console window indicate the available memory in EdgeLock SE05x security IC (in this case, 592) as can be seen in [Figure 23](#):

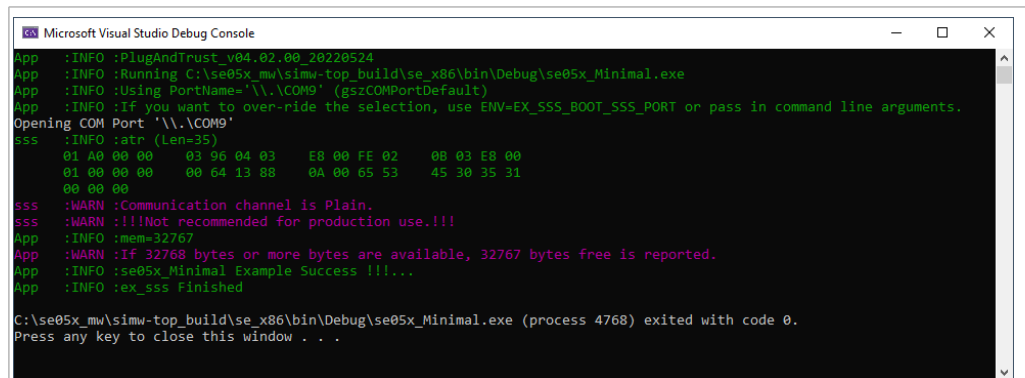


Figure 23. Verify that `se05x_minimal` project is running

- The same operation can be repeated with any of the other Plug & Trust middleware project examples.

5 Product specific CMake build settings

The NXP Plug & Trust middleware supports the SE05x Secure Elements, the A5000 Secure Authenticator, and the legacy A71CH products.

The EdgeLock Plug & Trust middleware is delivered with CMake files that include the set of directives and instructions describing the project's source files and the build targets. The CMake files are used to select a dedicated EdgeLock product IC and the corresponding IoT applet or Authenticator application.

The SE050 product identification can be obtained as described in [AN12436](#) chapter 1 *Product Information*. [AN12973](#) describes the same procedure for the SE051 product family.

The following tables show the required PTMW CMake options to build a dedicated product variant. The SSSFTR_SE05X_RSA CMake option is used to optimize the memory footprint for product variants that do not support RSA.

Table 3. CMake Settings for SE050E product variants

Variant	OEF ID	PTMW_Applet	PTMW_FIPS	PTMW_SE05X_Ver	PTMW_SE05X_Auth	PTMW_SCP	SSSFTR_SE05X_RSA
SE050E Dev. Board OM-SE050ARD-E	A921	SE050_E	None	07_02	any option	None or SCP03_SSS	disabled
SE050E2	A921						

Table 4. CMake Settings for SE050F product variants

Variant	OEF ID	PTMW_Applet	PTMW_FIPS	PTMW_SE05X_Ver	PTMW_SE05X_Auth	PTMW_SCP	SSSFTR_SE05X_RSA
SE050F Dev.Board OM-SE050ARD-F	A92A	SE05X_C	SE050	03_XX	PlatfSCP03 or UserID_PlatfSCP03 or AESKey_PlatfSCP03 or ECKey_PlatfSCP03	SCP03_SSS	enabled
SE050F2	A92A						

Table 5. CMake Settings for SE050 Previous Generation product variants

Variant	OEF ID	PTMW_Applet	PTMW_FIPS	PTMW_SE05X_Ver	PTMW_SE05X_Auth	PTMW_SCP	SSSFTR_SE05X_RSA
SE050A1	A204	SE05X_A	None	03_XX	any option	None or SCP03_SSS	disabled
SE050A2	A205						
SE050B1	A202	SE05X_B	None	03_XX	any option	None or SCP03_SSS	enabled
SE050B2	A203						

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Table 5. CMake Settings for SE050 Previous Generation product variants...continued

Variant	OEF ID	PTMW_Applet	PTMW_FIPS	PTMW_SE05X_Ver	PTMW_SE05X_Auth	PTMW_SCP	SSSFTR_SE05X_RSA
SE050C1	A200	SE05X_C	None	03_XX	any option	None or SCP03_SSS	enabled
SE050C2	A201						
SE050 Dev Board OM-SE050ARD	A1F4						
SE050F2	A77E ^[1]	SE05X_C	SE050	03_XX	PlatfSCP03 or UserID_PlatfSCP03 or AESKey_PlatfSCP03 or ECKey_PlatfSCP03	SCP03_SSS	enabled

[1] All SE050F2 with variant A77E have date code in year 2021. All the SE050F2 with date code in the year 2022 have the variant identifier A92A.

Table 6. CMake Settings for SE051 product variants

Variant	OEF ID	PTMW_Applet	PTMW_FIPS	PTMW_SE05X_Ver	PTMW_SE05X_Auth	PTMW_SCP	SSSFTR_SE05X_RSA
SE051A2	A920	SE05X_A	None	07_02	any option	None or SCP03_SSS	disabled
SE051C2	A8FA	SE05X_C	None	07_02	any option	None or SCP03_SSS	enabled
SE051W2	A739	SE05X_C	None	07_02	any option	None or SCP03_SSS or SCP03_SSS	enabled
SE051A2	A565	SE05X_A	None	06_00	any option	None or SCP03_SSS	disabled
SE051C2	A564	SE05X_C	None	06_00	any option	None or SCP03_SSS	enabled

Table 7. CMake Settings for A5000 product variants

Variant	OEF ID	PTMW_Applet	PTMW_FIPS	PTMW_SE05X_Ver	PTMW_SE05X_Auth	PTMW_SCP	SSSFTR_SE05X_RSA
OM-A5000ARD	A736	AUTH	None	07_02	any option	None	disabled
A5000	A736					or SCP03_SSS	

5.1 Example: SE050E CMake build settings

The following images show the configuration for the SE050E development board OM-SE05ARD-E.

- Select SE05X_E for the CMake option PTMW_Applet.
- Select None for the CMake option PTMW_FIPS.
- Select 07_02 for the CMake option PTMW_SE05X_Ver.
- Disable the CMake option SSSFTR_SE05X_RSA.

In this example we use plain communication. Plain communication for the example execution is enabled by selecting the following options:

- Select None for the CMake option PTMW_SE05X_Auth.
- Select None for the CMake option PTMW_SCP.

How to enable Platform SCP is described in [How to enable Platform SCP](#).

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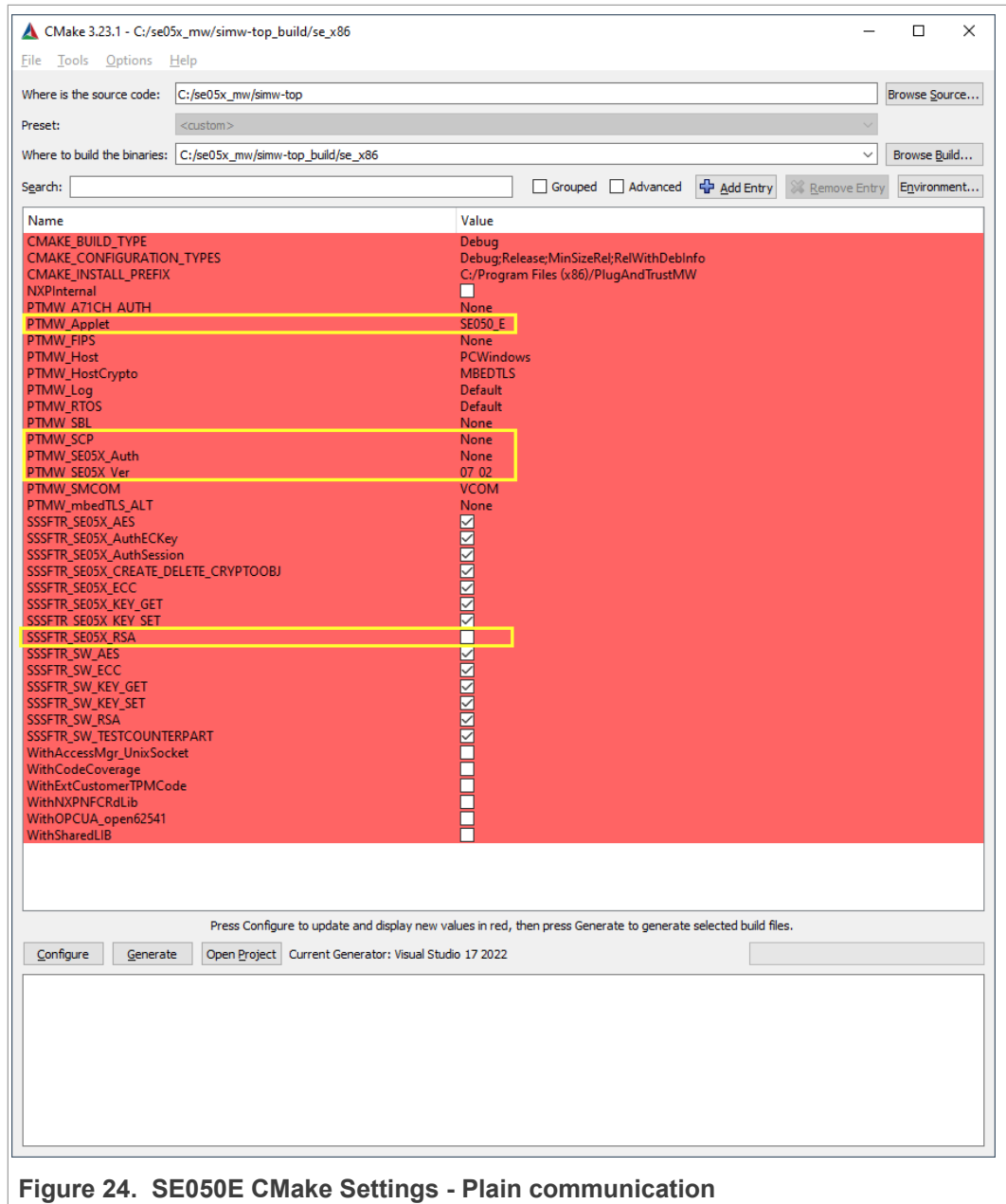


Figure 24. SE050E CMake Settings - Plain communication

6 Binding EdgeLock SE05x to a host using Platform SCP

Binding is a process to establish a pairing between the IoT device host MPU/MCU and EdgeLock SE05x, so that only the paired MPU/MCU is able to use the services offered by the corresponding EdgeLock SE05x and vice versa.

A mutually authenticated, encrypted channel will ensure that both parties are indeed communicating with the intended recipients and that local communication is protected against local attacks, including man-in-the-middle attacks aimed at intercepting the communication between the MPU/MCU and the EdgeLock SE05x and physical tampering attacks aimed at replacing the host MPU/MCU or EdgeLock SE05x .

EdgeLock SE05x natively supports Global Platform Secure Channel Protocol 03 (SCP03) for this purpose. PlatformSCP uses SCP03 and can be enabled to be mandatory.

This chapter describes the required steps to enable Platform SCP in the middleware for EdgeLock SE05x.

The following topics are discussed:

- [Section 6.1](#) Introduction to the Global Platform Secure Channel Protocol 03 (SCP03)
- [Section 6.2](#) How to configure the Platform SCP keys
- [Section 6.3](#) How to enable Platform SCP

6.1 Introduction to the Global Platform Secure Channel Protocol 03 (SCP03)

The Secure Channel Protocol SCP03 authenticates and protects locally the bidirectional communication between host and EdgeLock SE05x against eavesdropping on the physical I2C interface.

EdgeLock SE05x can be bound to the host by injecting in both the host and EdgeLock SE05x the same unique SCP03 AES key-set and by enabling the Platform SCP feature in the Plug & Trust middleware. The [AN12662](#) *Binding a host device to EdgeLock SE05x* describes in detail the concept of secure binding.

SCP03 is defined in [Global Platform Secure Channel Protocol '03' - Amendment D v1.2](#) specification.

SCP03 can provide the following three security goals:

- **Mutual authentication (MA)**
 - Mutual authentication is achieved through the process of initiating a Secure Channel and provides assurance to both the host and the EdgeLock SE05x entity that they are communicating with an authenticated entity.
- **Message Integrity**
 - The Command- and Response-MAC are generated by applying the CMAC according NIST SP 800-38B.
- **Confidentiality**
 - The message data field is encrypted across the entire data field of the command message to be transmitted to the EdgeLock SE05x, and across the response transmitted from the EdgeLock SE05x.

The SCP03 secure channel is set up via the EdgeLock SE05x Java Card OS Manager using the standard ISO7816-4 secure channel APDUs.

The establishment of an SCP03 channel requires three static 128-bit AES keys shared between the two communicating parties: *Key-ENC*, *Key-MAC* and *Key-DEK*. These keys are stored in the Java Card Secondary Security Domain (SSD) and not in the secure authenticator applet.

Key-ENC and *Key-MAC* keys are used during the SCP03 channel establishment to generate the session keys. Session Keys are generated to ensure that a different set of keys are used for each Secure Channel Session to prevent replay attacks.

Key-ENC is used to derive the session key *S-ENC*. The *S-ENC* key is used for encryption/decryption of the exchanged data. The session keys *S-MAC* and *R-MAC* are derived from *Key-MAC* and used to generate/verify the integrity of the exchanged data (C-APDU and R-APDU).

Key-DEK key is used to encrypt new SCP03 keys in case they get updated.

Table 8. Static SCP03 keys

Key	Description	Usage	Key Type
<i>Key-ENC</i>	Static Secure Channel Encryption Key	Generate session key for Decryption/Encryption (AES)	AES 128
<i>Key-MAC</i>	Static Secure Channel Message Authentication Code Key	Generate session key for Secure Channel authentication and Secure Channel MAC Verification/Generation (AES)	AES 128
<i>Key-DEK</i>	Data Encryption Key	Sensitive Data Decryption (AES)	AES 128

The session key generation is performed by the Plug & Trust middleware host crypto.

Table 9. SCP03 session keys

Key	Description	Usage	Key Type
<i>S-ENC</i>	Session Secure Channel Encryption Key	Used for data confidentiality	AES 128
<i>S-MAC</i>	Secure Channel Message Authentication Code Key for Command	Used for data and protocol integrity	AES 128
<i>S-RMAC</i>	Secure Channel Message Authentication Code Key for Response	User for data and protocol integrity	AES 128

Note: For further details please refer to [Global Platform Secure Channel Protocol '03' - Amendment D v1.2.](#)

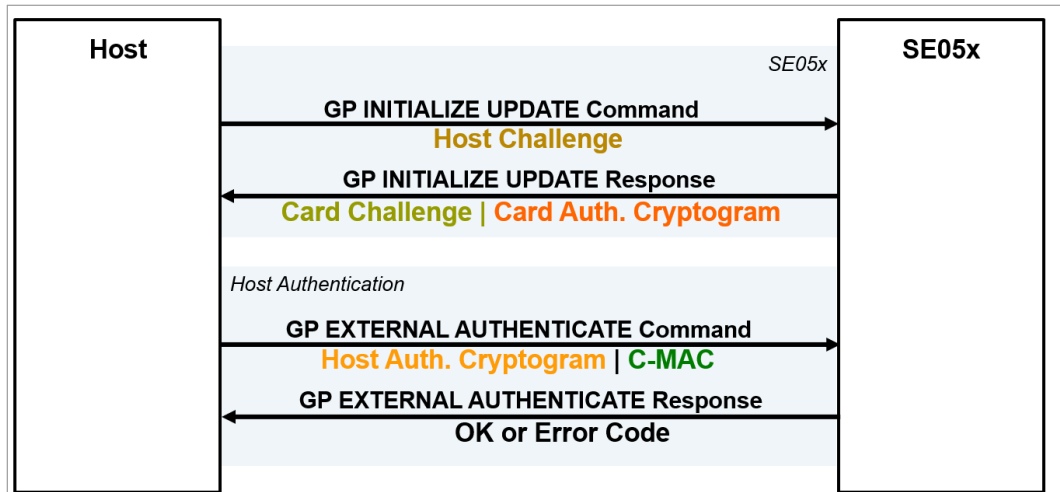


Figure 25. SPC03 mutual authentication – principle

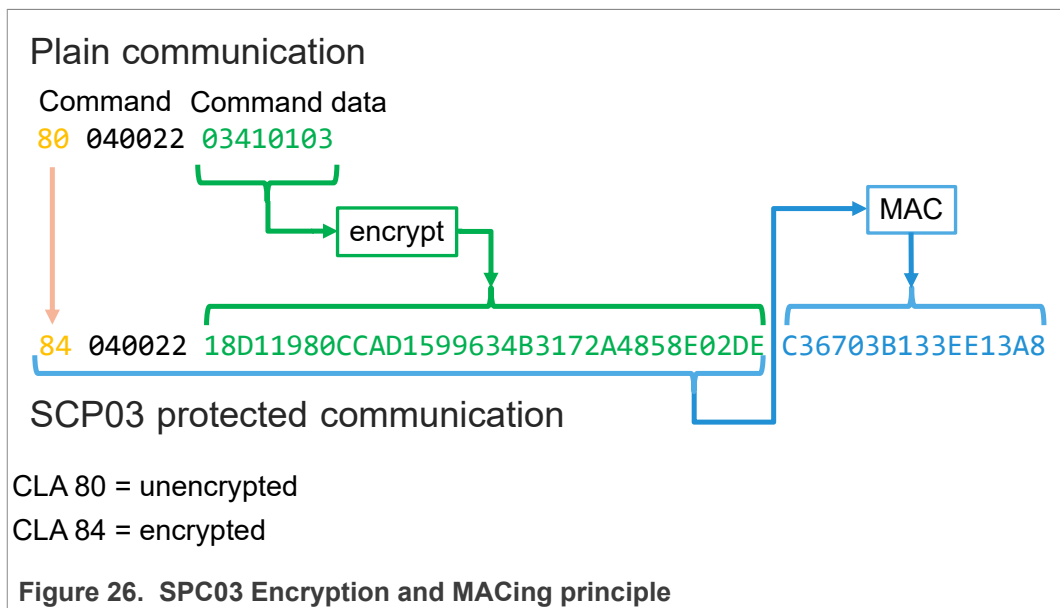


Figure 26. SPC03 Encryption and MACing principle

6.2 How to configure the product specific default Platform SCP keys

The default Platform SCP key values are described for the EdgeLock SE05x product variants in [AN12436](#) and for the EdgeLock SE051 variants in [AN12973](#).

The Platform SCP keys can be defined in the Plug & Trust middleware source code.

The Plug & Trust middleware header file `ex_sss_tp_scp03_keys.h` contains the default values of all EdgeLock SE05x, EdgeLock SE051, A5000 and A71CH product variants.

The `ex_sss_tp_scp03_keys.h` header file can be found in the following location: `C:\se05x_mw\simw-top\sss\ex\inc`

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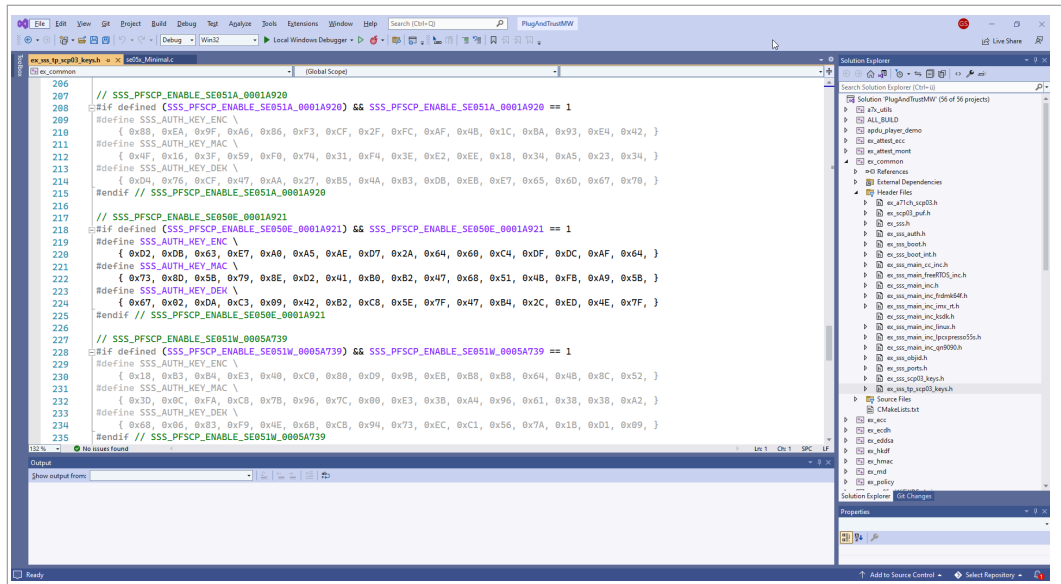


Figure 27. Default Platform SCP keys are defined in ex_sss_tp_scp03_keys.h

The fsl_sss_ftr.h.in file includes options to select one of the predefined default Platform SCP keys. This file is located in: C:\se05x_mw\simw-top\sss\inc

Select the desired value of the compilation option by setting exclusively the corresponding C-preprocessor define SSS_PFSCP_ENABLE_XX to 1 (enable). All other values for the same option (represented by C-preprocessor defines SSS_PFSCP_ENABLE_XX) must be set to 0.

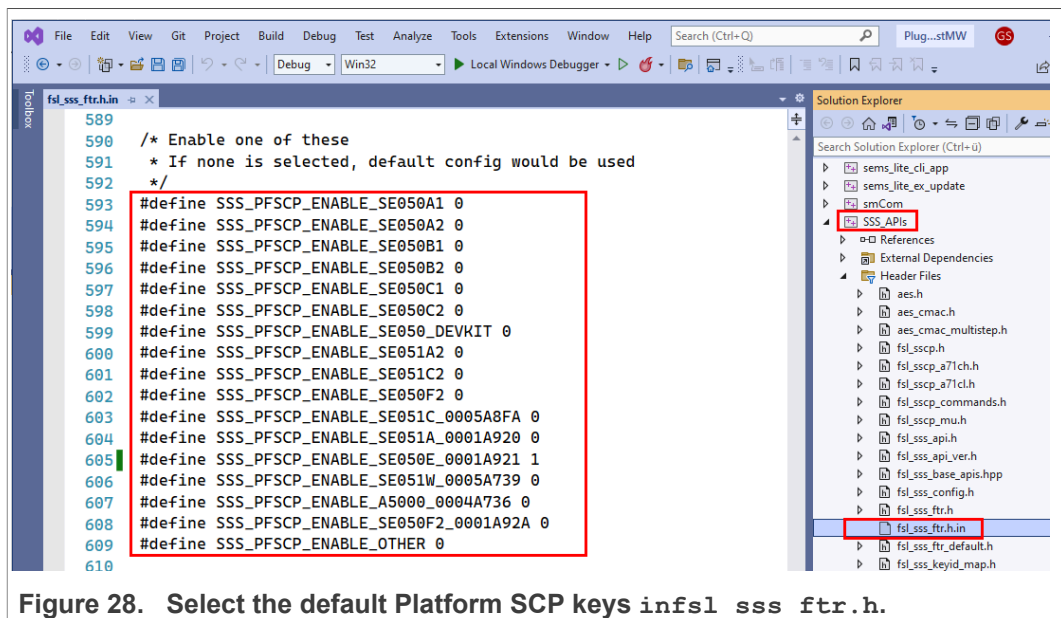


Figure 28. Select the default Platform SCP keys in fsl_sss_ftr.h.

The Plug & Trust Middleware uses a feature file to select/detect used/enabled features within the middleware stack. The file fsl_sss_ftr.h is automatically generated into the used build directory. CMake is overwriting the fsl_sss_ftr.h file every time CMake is invoked. CMake is using the SCP key settings of the fsl_sss_ftr.h.in file as input to generate the the fsl_sss_ftr.h file. You do not have to manually edit

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the `fsl_sss_ftr.h` feature file. Selections from CMake edit cache would automatically make relevant updates into the generated feature file.

Note: The Platform SCP key selection in the `fsl_sss_ftr.h` in CMake input file is persistent.

The location of the generated `fsl_sss_ftr.h` feature header file is: `C:\se05x_mw\simw-top_build\se_x86`

The following tables contains the the Platform SCP key header file define to be set to 1 (enable) for the different secure element and secure authenticator product variants.

Table 10. Platform SCP key define prefix for SE050E product variants

Variant	OEF ID	Platform SCP key define to be set to '1'
SE050E Dev. Board OM-SE050ARD-E	A921	SSS_PFCSCP_ENABLE_SE050E_0001A921
SE050E2	A921	SSS_PFCSCP_ENABLE_SE050E_0001A921

Table 11. Platform SCP key define prefix for SE050F product variants

Variant	OEF ID	Platform SCP key define to be set to '1'
SE050F Dev.Board OM-SE050ARD-F	A92A	SSS_PFCSCP_ENABLE_SE050F2_0001A92A
SE050F2	A92A	SSS_PFCSCP_ENABLE_SE050F2_0001A92A

Table 12. Platform SCP key define prefix for SE050 Previous Generation product variants

Variant	OEF ID	Platform SCP key define to be set to '1'
SE050A1	A204	SSS_PFCSCP_ENABLE_SE050A1
SE050A2	A205	SSS_PFCSCP_ENABLE_SE050A2
SE050B1	A202	SSS_PFCSCP_ENABLE_SE050B1
SE050B2	A203	SSS_PFCSCP_ENABLE_SE050B2
SE050C1	A200	SSS_PFCSCP_ENABLE_SE050C1
SE050C2	A201	SSS_PFCSCP_ENABLE_SE050C2
SE050 Dev Board OM-SE050ARD	A1F4	SSS_PFCSCP_ENABLE_SE050_DEVKIT
SE050F2	A77E ^[1]	SSS_PFCSCP_ENABLE_SE050F2

[1] All SE050F2 with variant A77E have date code in year 2021. All the SE050F2 with date code in the year 2022 have the variant identifier A92A.

Table 13. Platform SCP key define prefix for SE051 product variants

Variant	OEF ID	Platform SCP key define to be set to '1'
SE051A2	A920	SSS_PFCSCP_ENABLE_SE051A_0001A920
SE051C2	A8FA	SSS_PFCSCP_ENABLE_SE051C_0005A8FA
SE051W2	A739	SSS_PFCSCP_ENABLE_SE051W_0005A739
SE051A2	A565	SSS_PFCSCP_ENABLE_SE051A2
SE051C2	A564	SSS_PFCSCP_ENABLE_SE051C2

Table 14. Platform SCP key define prefix for A5000 product variants

Variant	OEF ID	Platform SCP key define to be set to '1'
A5000 Dev. Board OM-A5000ARD	A736	SSS_PFSKP_ENABLE_A5000_0004A736
A5000	A736	SSS_PFSKP_ENABLE_A5000_0004A736

6.3 How to enable Platform SCP

To enable Platform SCP is required to rebuild the SDK with the following CMake options:

- Select `SCP03_SSS` for the CMake option `PTMW_SCP`.
- Select `PlatfSCP03` for the CMake option `PTMW_SE05X_Auth`.

The following images show the configuration for the SE050E development board OM-SE05ARD-E.

1. Open a command prompt and go to the directory where the Plug & Trust middleware is built.
Send: `cd C:\se05x_mw\simw-top_build\se_x86`
2. Open the cmake configuration interface.
Send: `cmake-gui .`

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Figure 29. SE050E CMake Settings - PlatformSCP enabled

If you have edited any of the parameters in the menu, before exiting press the buttons **Configure** and **Generate** to apply the changes. In the next step we need to rebuild the Visual Studio solution. Finally, we can verify if we successfully enabled Platform SCP. For this purpose we run again the se05x_minimal example as described in [Section 4.4](#).

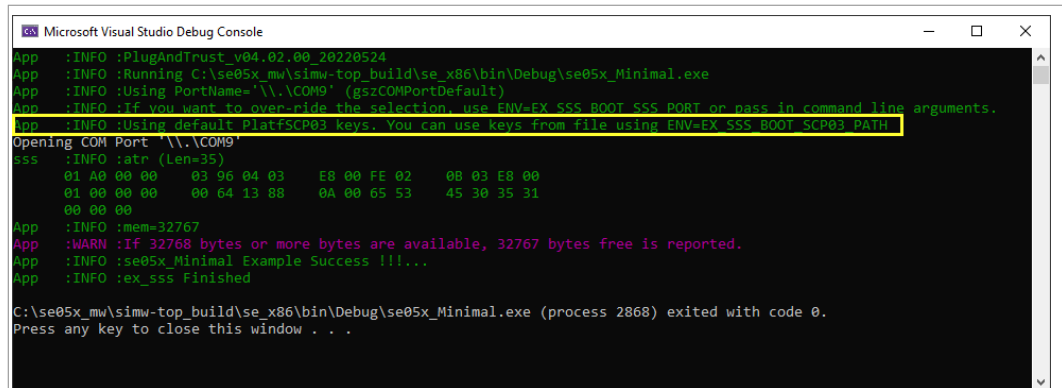


Figure 30. Verify that se05x_minimal project is running with Platform SCP enabled

The Plug & Trust Middleware provides the following additional examples to rotate the PlatformSCP Keys and to mandate Platform SCP.

- SE05X Rotate PlatformSCP Keys example:** Showcases authentication with default Platform SCP03 keys and the rotation (update) of those keys with user defined keys. The example documentation is available in the EdgeLock SE05x Plug & Trust Middleware documentation (C:\se05x_mw\simw-top\doc\demos\se05x\se05x_RotatePlatformSCP03Keys\Readme.html). The example source code is available at C:\se05x_mw\simw-top\demos\se05x\se05x_RotatePlatformSCP03Keys.
- SE05X Mandate SCP example:** Showcases how to make Platform SCP03 authentication mandatory in EdgeLock SE05x. The example documentation is available in the EdgeLock SE05x Plug & Trust Middleware documentation (C:\se05x_mw\simw-top\doc\demos\se05x\se05x_MandatePlatformSCP\Readme.html). The example source code is available at C:\se05x_mw\simw-top\demos\se05x\se05x_MandatePlatformSCP.
- SE05x AllowWithout PlatformSCP example:** This project demonstrates how to configure SE05X to allow without platform SCP. The example documentation is available in the EdgeLock SE05x Plug & Trust Middleware documentation (~\se_mw\simwtop\doc\demos\se05x\se05x_AllowWithoutPlatformSCP\Readme.html). The example source code is available at ~\se_mw\simw-top\demos\se05x\se05x_AllowWithoutPlatformSCP.

7 Appendix A: Install Visual Studio 2022

Visual Studio is Microsoft's fully-featured IDE for Android, iOS, Windows, web, and cloud. Visual Studio 2022 introduces rich support for CMake, including cross-platform CMake projects.

This section explains how to install Visual Studio 2022 version, but the same procedure can be applied for more recent versions.

1. Go to [Visual Studio](https://visualstudio.microsoft.com) site.
2. Select (1) **Download** and click on **Community 2022** in the *Download Visual Studio* button as shown in [Figure 31](#) and [Figure 32](#):

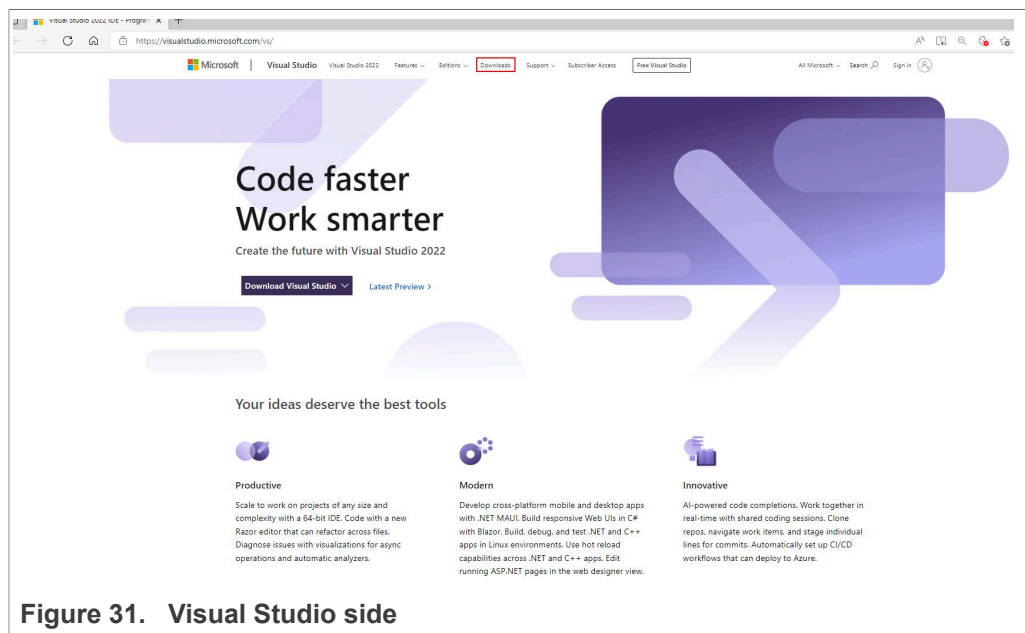


Figure 31. Visual Studio side

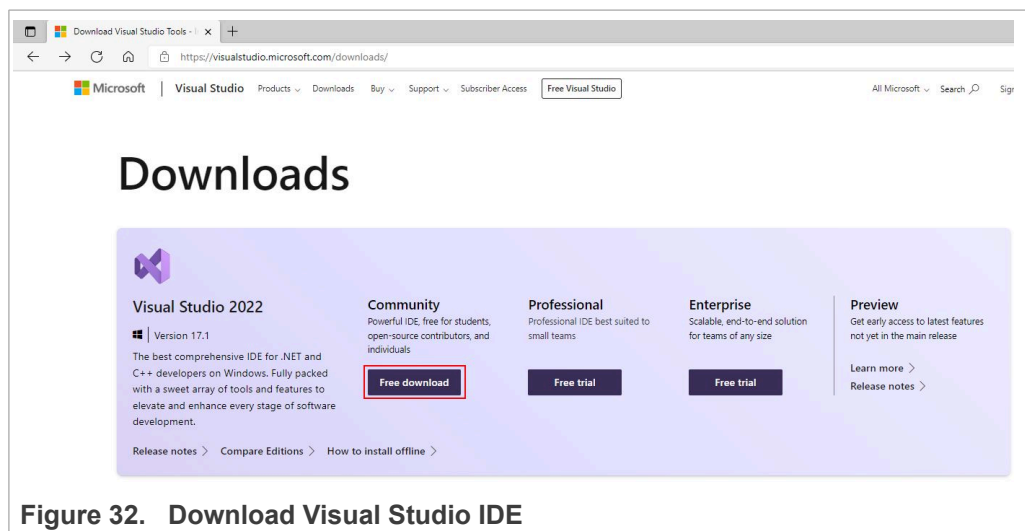


Figure 32. Download Visual Studio IDE

EdgeLock SE05x Quick start guide with Visual Studio project examples

- An *.exe installer will download to your laptop. Double click on the installer file and follow the setup wizard until the installation is completed. This process might take a few minutes. [Figure 33](#) shows Visual Studio installation wizard as an example:

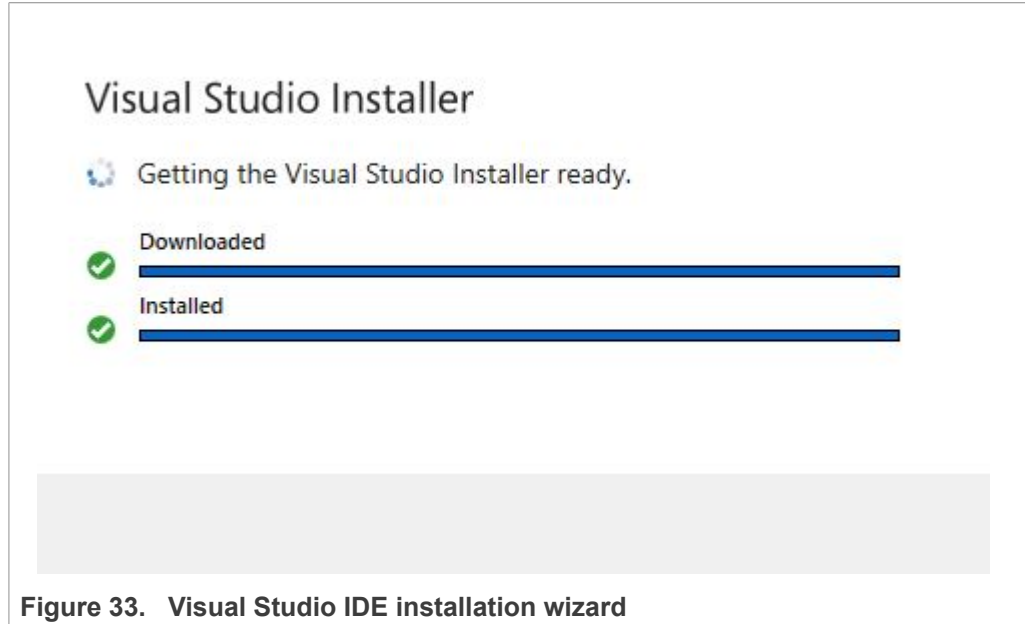


Figure 33. Visual Studio IDE installation wizard

- As part of the Visual Studio setup, it is mandatory that you enable the installation of **Desktop development with C++**. Select (1) **Desktop development with C++** and (2) click install as shown in [Figure 34](#):

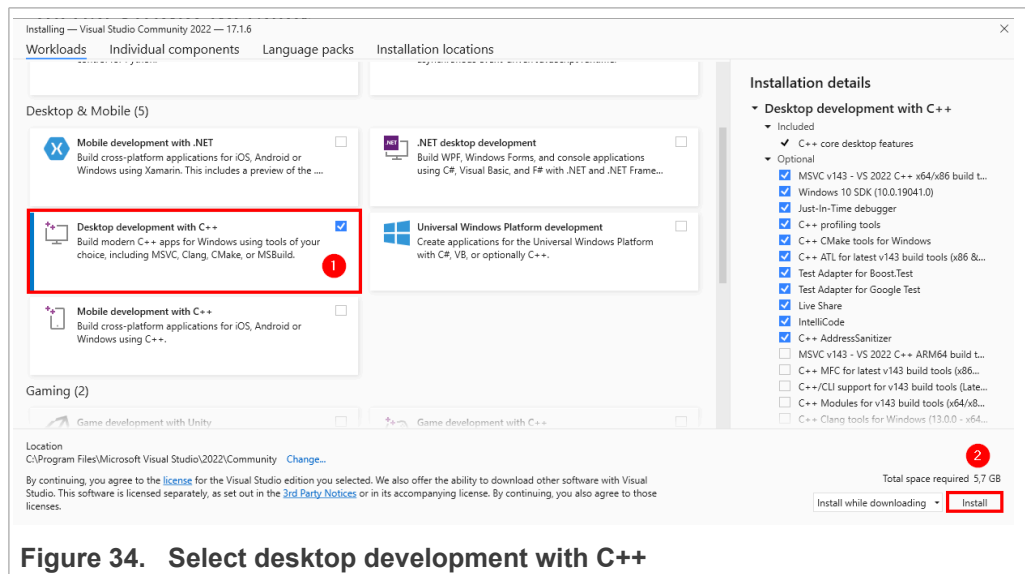


Figure 34. Select desktop development with C++

5. Visual C++ Tools for CMake is installed by default as part of the Desktop development with C++ workload. This process might take several minutes. [Figure 35](#) shows Visual Studio installation wizard as an example:

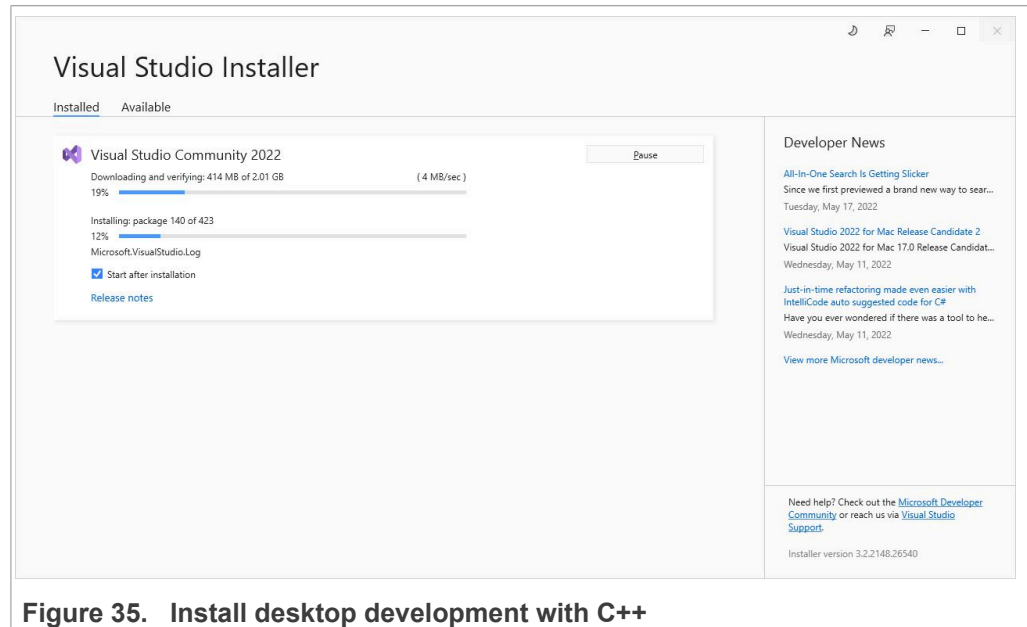


Figure 35. Install desktop development with C++

6. After the installation is completed, you might be asked to reboot your system.

8 Appendix B: Install CMake

CMake is an open-source, cross-platform family of tools that helps you build C/C++ projects on multiple platforms using a compiler-independent method. It has minimal dependencies, requiring only a C++ compiler on its own build system. SE05x middleware leverages on CMake to generate native makefiles and workspaces that can be used in the compiler environment of your choice.

To install CMake:

1. Go to CMake downloads page: <https://cmake.org/download/>
2. Scroll down and select your binary distribution. For this guide, the binary distribution is Windows as shown in [Figure 36](#):

The screenshot shows the CMake website's download page for version 3.22.3. It includes a navigation menu, a 'Latest Release (3.22.3)' section with introductory text, and two tables: 'Source distributions' and 'Binary distributions'. The 'Binary distributions' table lists various platform-specific installers and archives. A red arrow points to the 'cmake-3.22.3-windows-x86_64.msi' file in the 'Windows x64 Installer' row.

Platform	Files
Unix/Linux Source (has \n line feeds)	cmake-3.22.3.tar.gz
Windows Source (has \r\n line feeds)	cmake-3.22.3.zip

Platform	Files
Windows x64 Installer: Installer tool has changed. Uninstall CMake 3.4 or lower first!	cmake-3.22.3-windows-x86_64.msi
Windows x64 ZIP	cmake-3.22.3-windows-x86_64.zip
Windows i386 Installer: Installer tool has changed. Uninstall CMake 3.4 or lower first!	cmake-3.22.3-windows-i386.msi
Windows i386 ZIP	cmake-3.22.3-windows-i386.zip
macOS 10.13 or later	cmake-3.22.3-macos-universal.dmg
	cmake-3.22.3-macos-universal.tar.gz
macOS 10.10 or later	cmake-3.22.3-macos10.10-universal.dmg
	cmake-3.22.3-macos10.10-universal.tar.gz
Linux x86_64	cmake-3.22.3-linux-x86_64.sh
	cmake-3.22.3-linux-x86_64.tar.gz
Linux aarch64	cmake-3.22.3-linux-aarch64.sh
	cmake-3.22.3-linux-aarch64.tar.gz

Figure 36. Download CMake

- Double click on the downloaded installer file. Windows Defender SmartScreen might pop-up the wizard shown in [Figure 37](#):

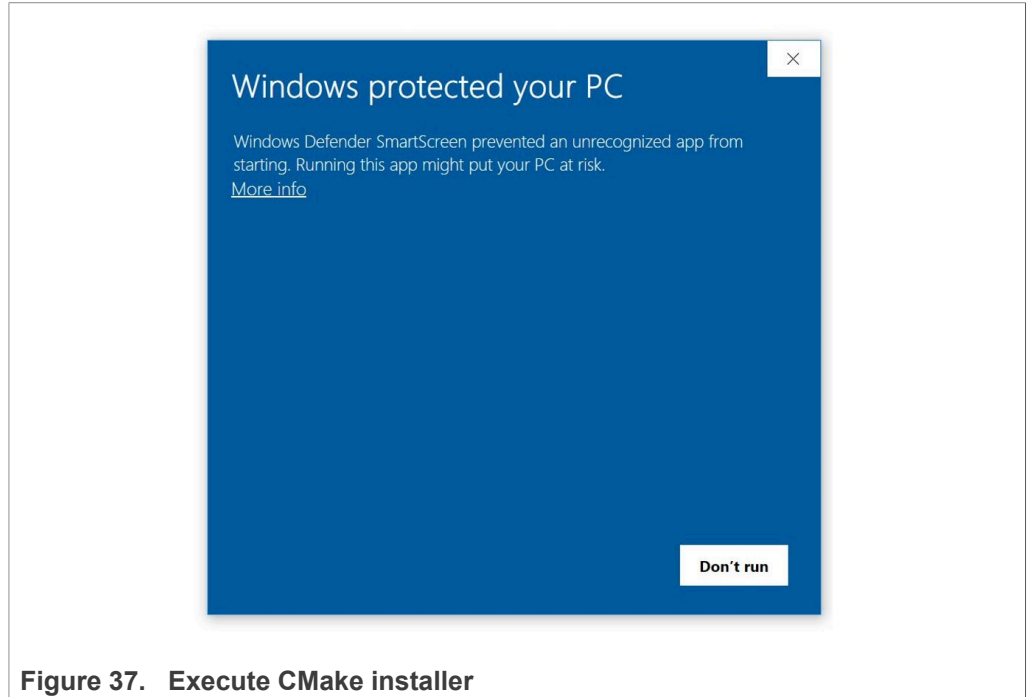


Figure 37. Execute CMake installer

- If this is your case: Click (1) on **More info** and then (2) click on **Run anyway** as shown in [Figure 38](#):

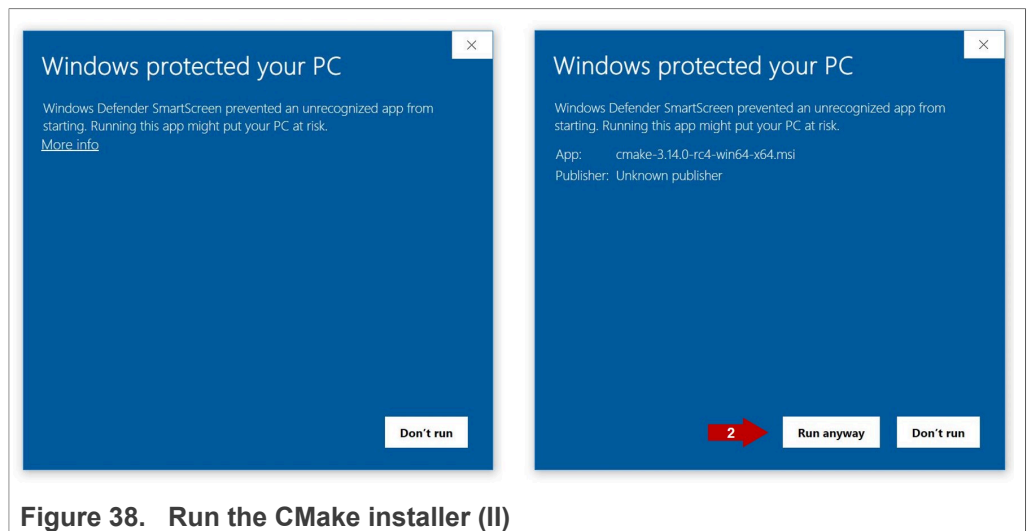


Figure 38. Run the CMake installer (II)

- The CMake installation wizard will open. Click (1) **Next** and (2) **accept** the End-User License Agreement as shown in [Figure 39](#):

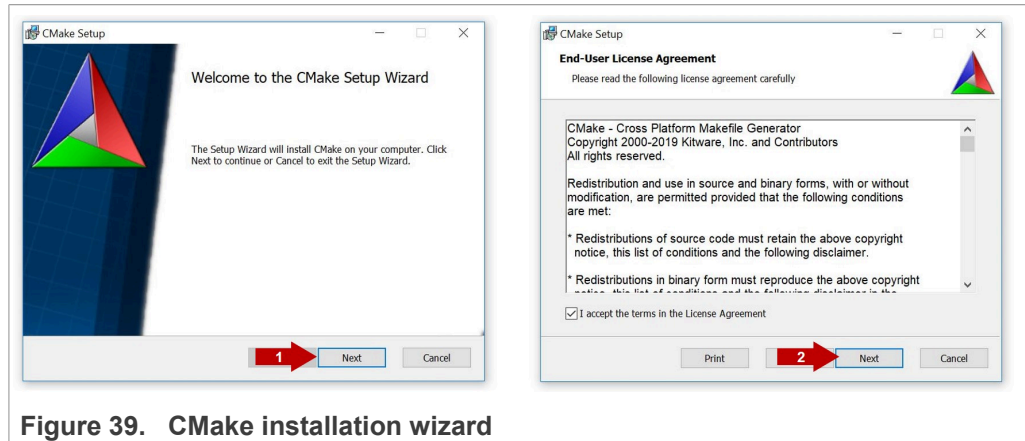


Figure 39. CMake installation wizard

- As part of the CMake setup, (1) **Add Cmake to the system PATH for all users** and (2) click **Next** as shown in [Figure 40](#):

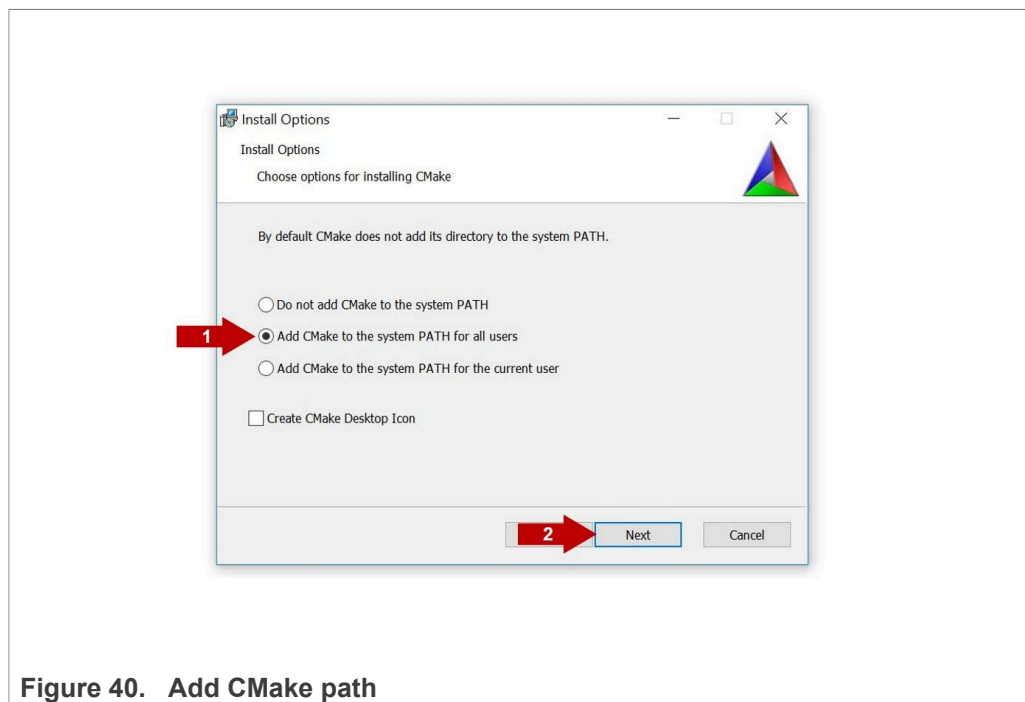
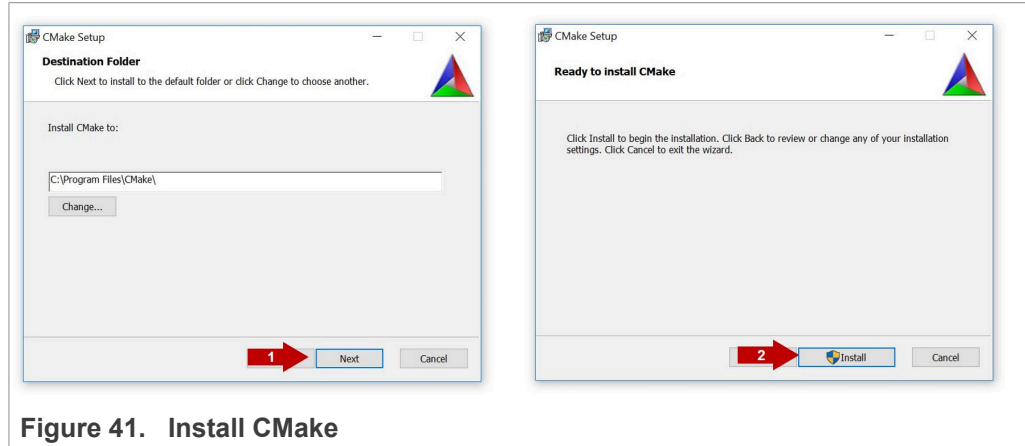
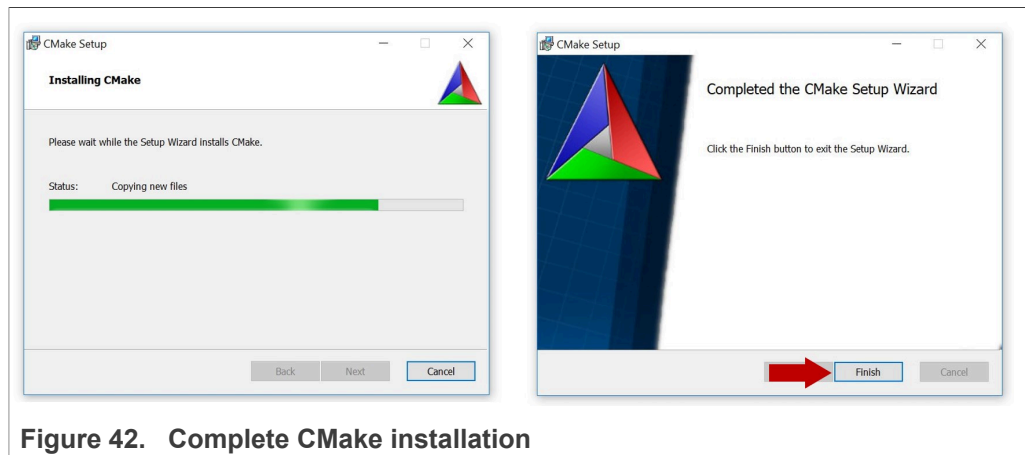


Figure 40. Add CMake path

7. Select a destination folder, (1) click **Next** and then (2) click **Install** as shown in [Figure 41](#):



8. Wait a few seconds until the installation is completed and click **Finish** as shown in [Figure 42](#):



9 Appendix C: Install Python

This section explains how to install Python $\geq 3.7.x$ and $\leq 3.9.x$ 32-bit version, but the same procedure can be applied for more recent versions. Follow these steps to install Python in your local machine:

1. Go to <https://www.python.org/downloads> and download **Python $\geq 3.7.x$ and ≤ 3.9 32-bit version**. Make sure you download the Python 32 bit version.

Version	Operating System	Description	MD5 Sum	File Size	PGP
Gzipped source tarball	Source release		1440acb71471e2394befdb30b1a958d1	25800844	SIG
XZ compressed source tarball	Source release		e754c4b2276750fd5b4785a1b443683a	19154136	SIG
macOS 64-bit Intel-only installer	macOS	for macOS 10.9 and later, deprecated	2714cb9e6241cf7e2f9022714a55d27a	30395760	SIG
macOS 64-bit universal2 installer	macOS	for macOS 10.9 and later	c2393ab11a423d817501b8566ab5da9f	38217233	SIG
Windows embeddable package (32-bit)	Windows		c1d2af96d9f3564f57f35fc3c1006eb	7671509	SIG
Windows embeddable package (64-bit)	Windows		b8e8bfa8e56edcd654d15e3bdc2e29a	8509821	SIG
Windows help file	Windows		784020441c1a25289483d3d8771a8215	9284044	SIG
Windows installer (32-bit)	Windows		457d648dc8a71b6bc32da30a7805c55b	27767040	SIG
Windows installer (64-bit)	Windows	Recommended	747ac35ae667f4ec1ee3b001e9b7dbc6	28909456	SIG

Figure 43. Download Python 3.9.x 32 bit version

2. Double click on the downloaded installer file. Select the "Install launcher for all users" and "Add Python 3.7 to Path" options and click *Install Now* as indicated in Figure 44:

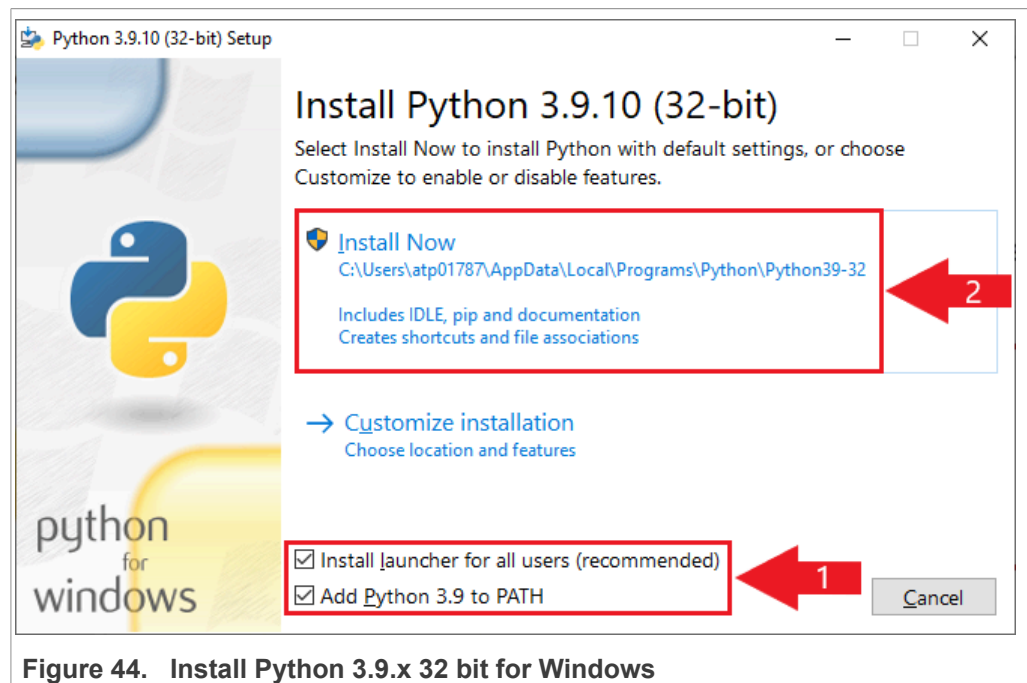


Figure 44. Install Python 3.9.x 32 bit for Windows

3. Wait a few seconds until the installation is completed as indicated in [Figure 45](#)

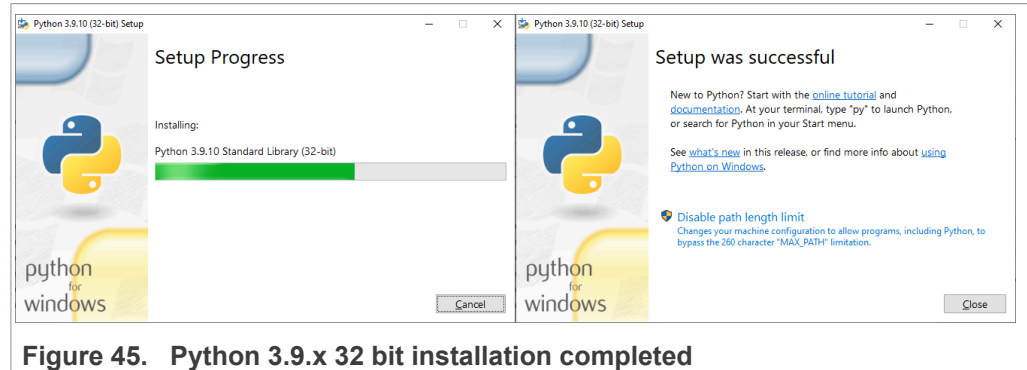


Figure 45. Python 3.9.x 32 bit installation completed

10 Appendix D: Update FRDM-K64F board with DAPLink firmware

Arm Mbed DAPLink is an open-source software project that enables programming and debugging application software running on Arm Cortex CPUs. DAPLink runs an open-source bootloader and enables developers with drag-and-drop programming, a serial port and CMSIS-DAP based debugging.

Note: To debug MCUXpresso project examples, we need to flash FRDM-K64F with DAPLink firmware. If your FRDM-K64F board already includes DAPLink firmware, you can skip these steps.

To flash DAPLink firmware, follow these steps:

1. Go to [NXP OpenSDA](#) site
2. Scroll down and select FRDM-K64F board from the **Download - OpenSDA bootloader and application** drop down list as indicated in [Figure 46](#):

The screenshot shows the NXP OpenSDA website interface. On the left, there are navigation links: 'Jump To', 'Download - OpenSDA Bootloader and Application', 'Overview & Features', and 'Comparison Table of Different OpenSDA Versions'. The main content area features a block diagram titled 'OpenSDA Block Diagram' which illustrates the connection between a 'USB Host' (containing IDE, File System, and Serial Terminal) and the 'OpenSDA Serial and Debug Adapter'. The adapter is connected to an 'OpenSDA' module containing an 'OpenSDA MCU K200X120Vxx5', an 'MSD Bootloader', and an 'OpenSDA Application'. A red arrow points from the 'FRDM-K64F' entry in a dropdown menu to the 'OpenSDA Application' component in the diagram. The dropdown menu is titled 'Download - OpenSDA Bootloader and Application' and lists various boards, with 'FRDM-K64F' selected. Below the dropdown, there is a button labeled 'To update your board with OpenSDA applications' and a 'Choose your board to start' dropdown.

Figure 46. DAPLink firmware update - select board

3. Download the latest DAPLink firmware version as shown in [Figure 47](#):

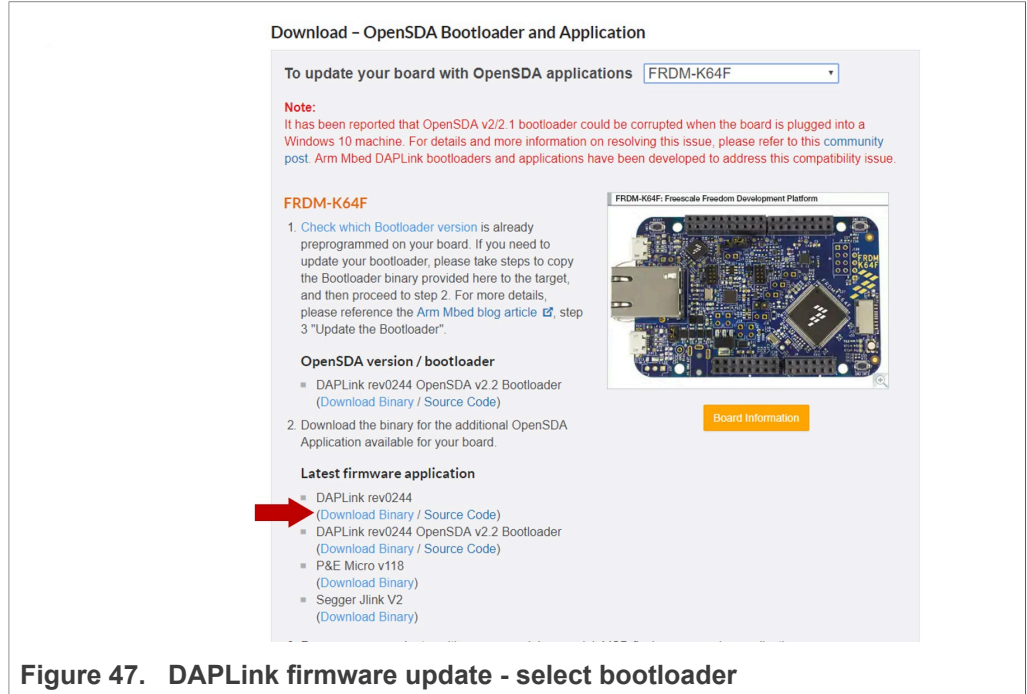


Figure 47. DAPLink firmware update - select bootloader

4. Start the board's bootloader mode. To do so, (1) keep reset button pressed while (2) connecting the USB cable to the SDA USB port and release it after 1s ([Figure 48](#)):

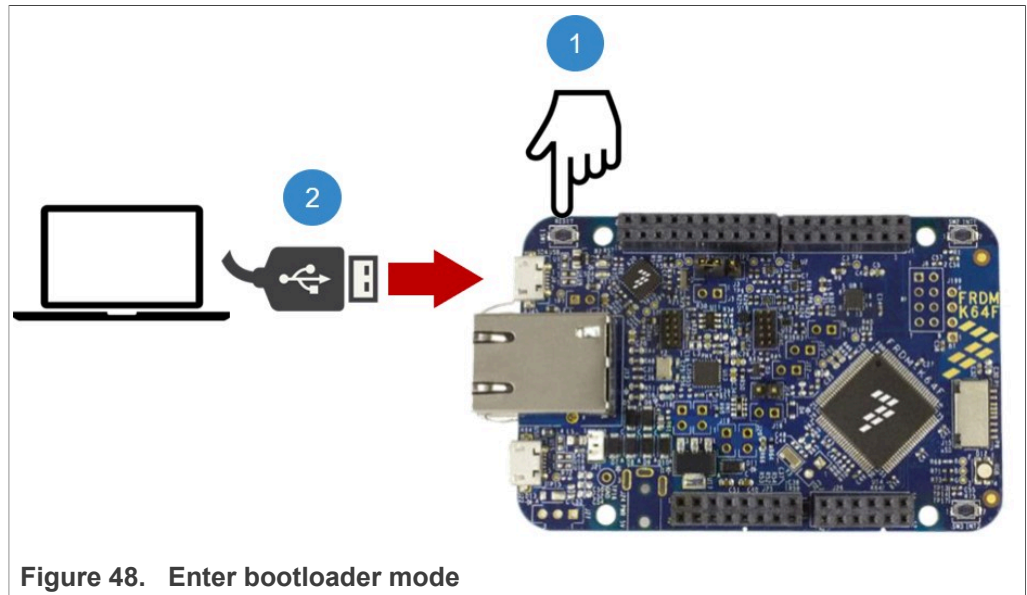


Figure 48. Enter bootloader mode

5. Drag and drop or copy and paste the binary file into the BOOTLOADER drive from your computer file explorer as shown in [Figure 49](#). The FRDM-K64F will automatically un-mount after the drag and drop operation.

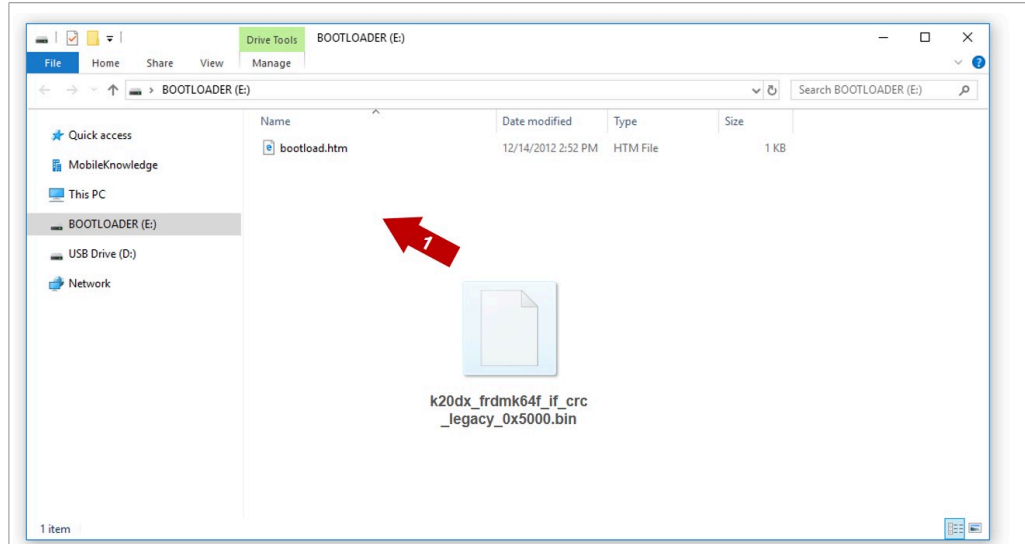


Figure 49. Enter bootloader mode

6. Un-plug and re-plug the USB cable from the SDA USB port **without** keeping reset button pressed.
7. Check the category Ports (COM & LPT) from your computer Device Manager to ensure that new devices have been properly detected and their driver correctly installed by your computer OS.

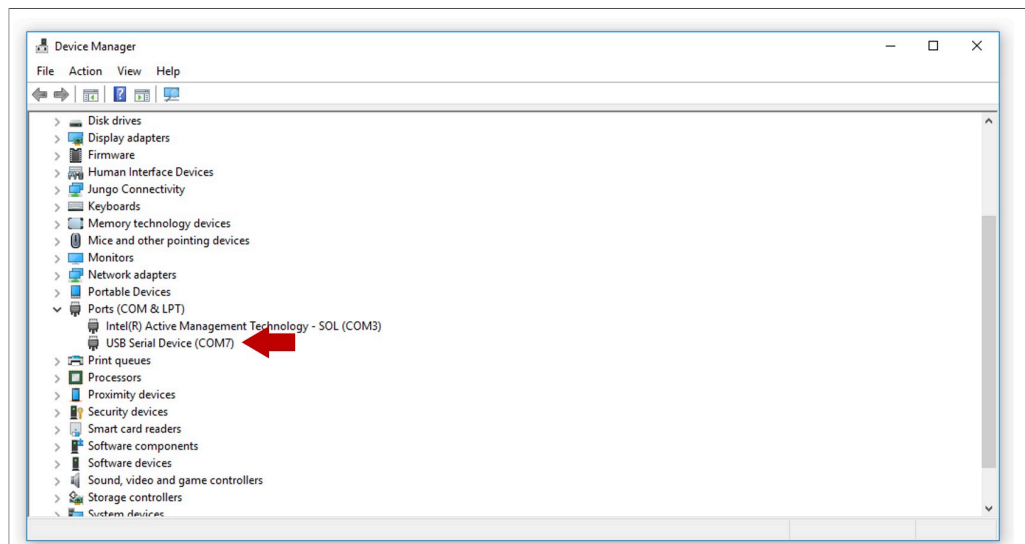


Figure 50. Enter bootloader mode

Note: In case the device does not show up in your Device Manager, please download the latest bootloader version, as shown in [Figure 47](#), or check / exchange the USB cables used.

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