



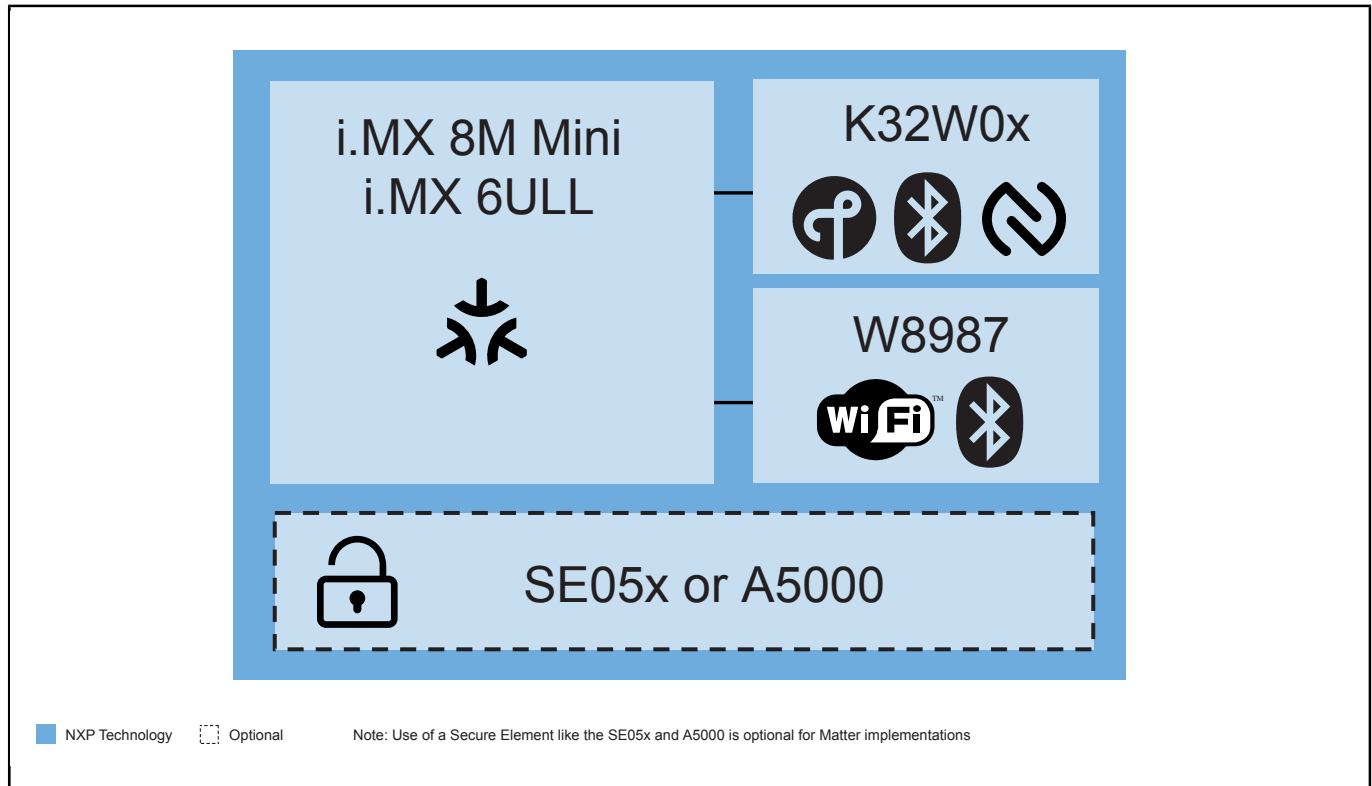
# MPU / Linux<sup>®</sup> Hosted Matter Development Platform

## MPU-LINUX-MATTER-DEV-PLATFORM

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NXP's MPU/Linux Hosted Matter Development platforms includes the hardware, software and enablement needed to create advanced end devices using Matter over Wi-Fi and Thread that act as mesh extenders, Matter controllers or Thread Border Routers. These Development Platforms implement a hosted architecture using a range of the i.MX Linux portfolio along with the radio chip needed to support the targeted use case.

## MPU / Linux Hosted Matter Development Platform Block Diagram



View additional information for [MPU / Linux® Hosted Matter Development Platform](#).

**Note:** The information on this document is subject to change without notice.

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